

Curriculum Vitae

Thomas A. Henzinger

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Coordinates

Computer and Communication Sciences (IC)	Phone: +41 21 693-5234
EPFL Station 14	Fax: +41 21 693-7540
CH-1015 Lausanne	Email: tah@epfl.ch
Switzerland	Web: mtc.epfl.ch/~tah

Research Interests

Mathematical logic, automata theory, and semantics of computation.
Modeling and analysis of reactive, real-time, and hybrid systems.
Formal software and hardware verification, especially model checking.
Design and implementation of concurrent and embedded software.
Computational modeling and prediction of biological systems.

Education

September 1991	Ph.D., Computer Science	Stanford University
July 1987	Dipl.-Ing., Computer Science	University of Linz
August 1986	M.S., Computer and Information Sciences	University of Delaware

Academic Employment

since July 2005	Adjunct Professor, Electrical Engineering and Computer Sciences	University of California, Berkeley
since April 2004	Professor, Computer and Communication Sciences	EPFL
January 1999 to March 2000	Director	Max-Planck Institute for Computer Science, Saarbrücken
July 1998 to June 2005	Professor, Electrical Engineering and Computer Sciences	University of California, Berkeley
July 1997 to June 1998	Associate Professor, Electrical Engineering and Computer Sciences	University of California, Berkeley
January 1996 to June 1997	Assistant Professor, Electrical Engineering and Computer Sciences	University of California, Berkeley
January 1992 to December 1996	Assistant Professor, Computer Science	Cornell University
October 1991 to December 1991	Postdoctoral Scientist, IMAG Laboratory	University of Grenoble

Honors

Corresponding Member, Austrian Academy of Sciences, 2009.
ISI Highly Cited Researcher, 2006.
ACM Fellow, 2006.
IEEE Fellow, 2006.
Member, Academia Europaea, 2006.
Best Paper Award, ACM Symposium on Foundations of Software Engineering, 2006.
Member, German Academy of Sciences (Leopoldina), 2005.
Best Paper Award, International Conference on Concurrency Theory, 2001.
Honorary Professor of Computer Science, University of the Saarland, 1999.
ONR Young Investigator Award, 1995.
NSF Faculty Early Career Development Award, 1995.
Nominee for ACM Best Dissertation Award, Stanford University, 1992.
Graduation with *Distinction in Teaching*, Stanford University, 1991.
Forsythe Memorial Award for Excellence in Student Teaching, Stanford University, 1989.
IBM Graduate Fellow, 1988–1991.
Graduation with *Distinction*, University of Linz, 1987.
Fulbright Fellow, 1985–1986.

Doctoral Students

1. Vinayak S. Prabhu (Ph.D., University of California, Berkeley, 2008):
Games for the Verification of Timed Systems.
2. Slobodan Matic (Ph.D., University of California, Berkeley, 2008):
Compositionality in Deterministic Real-Time Embedded Systems.
3. Johannes Borgström (Ph.D., EPFL, 2008):
Equivalences and Calculi for the Formal Verification of Cryptographic Protocols
(jointly supervised with Uwe Nestmann).
4. Arkadeb Ghosal (Ph.D., University of California, Berkeley, 2008):
A Hierarchical Coordination Language for Reliable Real-Time Tasks
(jointly supervised with Alberto Sangiovanni-Vincentelli).
5. Arindam Chakrabarti (Ph.D., University of California, Berkeley, 2007):
A Framework for Compositional Design and Analysis of Systems.
6. Krishnendu Chatterjee (Ph.D., University of California, Berkeley, 2007):
Stochastic Omega-Regular Games.
Won the Sakrison Memorial Award, University of California, Berkeley, 2008.
Won the Ackermann Award, European Association for Computer Science Logic, 2008.
7. Simon Kramer (Ph.D., EPFL, 2007):
Logical Concepts in Cryptography
(jointly supervised with Uwe Nestmann).
8. Ranjit Jhala (Ph.D., University of California, Berkeley, 2004):
Program Verification by Lazy Abstraction.
Won the Sakrison Memorial Award, University of California, Berkeley, 2005.

9. Rupak Majumdar (Ph.D., University of California, Berkeley, 2003):
Symbolic Algorithms for Verification and Control.
Won the Chua Award, University of California, Berkeley, 2002.
10. Benjamin Horowitz (Ph.D., University of California, Berkeley, 2003):
GIOTTO: A Time-triggered Language for Embedded Programming.
11. Freddy Y.C. Mang (Ph.D., University of California, Berkeley, 2002):
Games in Open Systems Verification and Synthesis.
12. Shaz Qadeer (Ph.D., University of California, Berkeley, 1999):
Algorithms and Methodology for Scalable Model Checking.
13. Sriram K. Rajamani (Ph.D., University of California, Berkeley, 1999):
New Directions in Refinement Checking.
14. Peter W. Kopke (Ph.D., Cornell University, 1996):
The Theory of Rectangular Hybrid Automata.
15. Pei-Hsin Ho (Ph.D., Cornell University, 1995):
Automatic Analysis of Hybrid Systems.

Current doctoral students (EPFL): Maria Mateescu, Tatjana Petrov, Vasu Singh, Anmol Tomar, Grégory Théoduloz (won a Microsoft Postgraduate Research Studentship).

Software

1. BLAST, a model checker for C programs (since 2001).
2. CHIC, a JBuilder plug-in for interface automata (2001–2004).
3. GIOTTO, a coordination language for distributed real-time tasks (2001–2004).
4. MOCHA, a verification tool suite for reactive modules (1997–2000).
5. HYTECH, a symbolic model checker for hybrid systems (1993–1996).
6. PROOFPAD, an interactive proof editor (1984–1985).

Visiting Positions

1. Microsoft Research, Redmond, Washington, August 2005; August 2006.
2. Department of Information Technology and Electrical Engineering, ETH Zürich, June 2002.
3. Department of Computer Science, Technical University Munich, May 2002.
4. Department of Computer Science, Stanford University, July 2001 to April 2002.
5. AT&T Bell Laboratories, Murray Hill, New Jersey, summers 1991–1995.
6. Department of Applied Mathematics, Weizmann Institute of Science, March to June 1989; July 1990.

Publications

PDF files for all publications can be found at mtc.epfl.ch/~tah.

Refereed Conference Papers

1. Rachid Guerraoui, Thomas A. Henzinger, and Vasu Singh, “Software transactional memory on relaxed memory models,” *Proceedings of the 21st International Conference on Computer-Aided Verification (CAV)*, Lecture Notes in Computer Science 5643, Springer, 2009, pp. 321–336.
2. Thomas A. Henzinger, Maria Mateescu, and Verena Wolf, “Sliding window abstraction for infinite Markov chains,” *Proceedings of the 21st International Conference on Computer-Aided Verification (CAV)*, Lecture Notes in Computer Science 5643, Springer, 2009, pp. 337–352.
3. Roderick Bloem, Krishnendu Chatterjee, Thomas A. Henzinger, and Barbara Jobstmann, “Better quality in synthesis through quantitative objectives,” *Proceedings of the 21st International Conference on Computer-Aided Verification (CAV)*, Lecture Notes in Computer Science 5643, Springer, 2009, pp. 140–156.
4. Dietmar Berwanger, Krishnendu Chatterjee, Martin De Wulf, Laurent Doyen, and Thomas A. Henzinger, “ALPAGA: A tool for solving parity games with imperfect information,” *Proceedings of the 15th International Conference on Tools and Algorithms for the Construction and Analysis of Systems (TACAS)*, Lecture Notes in Computer Science 5505, Springer, 2009, pp. 58–61.
5. Krishnendu Chatterjee, Luca de Alfaro, and Thomas A. Henzinger, “Termination criteria for solving concurrent safety and reachability games,” *Proceedings of the 20th Annual Symposium on Discrete Algorithms (SODA)*, ACM Press, 2009, pp. 197–206.
6. Thomas A. Henzinger, Thibaud Hottelier, and Laura Kovács, “VALIGATOR: A verification tool with bound and invariant generation,” *Proceedings of the 15th International Conference on Logic for Programming, Artificial Intelligence, and Reasoning (LPAR)*, Lecture Notes in Computer Science 5330, Springer, 2008, pp. 333–342.
7. Rachid Guerraoui, Thomas A. Henzinger, and Vasu Singh, “Permissiveness in transactional memories,” *Proceedings of the 22nd International Symposium on Distributed Computing (DISC)*, Lecture Notes in Computer Science 5218, Springer, 2008, pp. 305–319.
8. Krishnendu Chatterjee, Thomas A. Henzinger, and Vinayak S. Prabhu, “Timed parity games: Complexity and robustness,” *Proceedings of the Sixth International Conference on Formal Modeling and Analysis of Timed Systems (FORMATS)*, Lecture Notes in Computer Science 5215, Springer, 2008, pp. 124–140.
9. Laurent Doyen, Thomas A. Henzinger, Barbara Jobstmann, and Tatjana Petrov, “Interface theories with component reuse,” *Proceedings of the Eighth Annual Conference on Embedded Software (EMSOFT)*, ACM Press, 2008, pp. 79–88.
10. Dirk Beyer, Thomas A. Henzinger, and Grégory Théoduloz, “Program analysis with dynamic change of precision,” *Proceedings of the 23rd International Conference on Automated Software Engineering (ASE)*, ACM Press, 2008, pp. 29–38.
11. Krishnendu Chatterjee, Laurent Doyen, and Thomas A. Henzinger. “Quantitative languages,” *Proceedings of the 17th International Conference on Computer Science Logic (CSL)*, Lecture Notes in Computer Science 5213, Springer, 2008, pp. 385–400. Invited to a special issue of *Logical Methods in Computer Science* for selected papers of CSL 08.
12. Dietmar Berwanger, Krishnendu Chatterjee, Laurent Doyen, Thomas A. Henzinger, and Sangram Raje, “Strategy construction for parity games with imperfect information,” *Proceedings of the 19th International Conference on Concurrency Theory (CONCUR)*, Lecture Notes in Computer Science 5201, Springer, 2008, pp. 325–339. Invited to a special issue of *Information and Computation* for selected papers of CONCUR 08.

13. Krishnendu Chatterjee, Thomas A. Henzinger, and Barbara Jobstmann, “Environment assumptions for synthesis,” *Proceedings of the 19th International Conference on Concurrency Theory (CONCUR)*, Lecture Notes in Computer Science 5201, Springer, 2008, pp. 147–161.
14. Rachid Guerraoui, Thomas A. Henzinger, and Vasu Singh, “Completeness and nondeterminism in model checking transactional memories,” *Proceedings of the 19th International Conference on Concurrency Theory (CONCUR)*, Lecture Notes in Computer Science 5201, Springer, 2008, pp. 21–35. Invited to a special issue of *Distributed Computing* for selected papers of CONCUR 08.
15. Jasmin Fisher, Thomas A. Henzinger, Maria Mateescu, and Nir Piterman, “Bounded asynchrony: Concurrency for modeling cell-cell interactions,” *Proceedings of the First International Workshop on Formal Methods in Systems Biology (FMSB)*, Lecture Notes in Computer Science 5054, Springer, 2008, pp. 17–32.
16. Rachid Guerraoui, Thomas A. Henzinger, Barbara Jobstmann, and Vasu Singh, “Model checking transactional memories,” *Proceedings of the International Conference on Programming Language Design and Implementation (PLDI)*, ACM Press, 2008, pp. 372–382. Also presented at the *Workshop on Exploiting Concurrency Efficiently and Correctly*, Princeton, New Jersey, July 2008.
17. Krishnendu Chatterjee, Arkadeb Ghosal, Thomas A. Henzinger, Daniel Ierican, Christoph M. Kirsch, Claudio Pinello, and Alberto Sangiovanni-Vincentelli, “Logical reliability of interacting real-time tasks,” *Proceedings of the International Conference on Design, Automation, and Test in Europe (DATE)*, IEEE Press, 2008, pp. 909–914.
18. Krishnendu Chatterjee, Thomas A. Henzinger, and Vinayak S. Prabhu, “Trading infinite memory for uniform randomness in timed games,” *Proceedings of the 11th International Workshop on Hybrid Systems: Computation and Control (HSCC)*, Lecture Notes in Computer Science 4981, Springer, 2008, pp. 87–100.
19. Krishnendu Chatterjee, Rupak Majumdar, and Thomas A. Henzinger, “Controller synthesis with budget constraints,” *Proceedings of the 11th International Workshop on Hybrid Systems: Computation and Control (HSCC)*, Lecture Notes in Computer Science 4981, Springer, 2008, pp. 72–86.
20. Krishnendu Chatterjee, Koushik Sen, and Thomas A. Henzinger, “Model checking omega-regular properties of interval Markov chains,” *Proceedings of the 11th International Conference on Foundations of Software Science and Computational Structures (FOSSACS)*, Lecture Notes in Computer Science 4962, Springer, 2008, pp. 302–317.
21. Ashutosh Gupta, Thomas A. Henzinger, Rupak Majumdar, Andrey Rybalchenko, and Ru-Gang Xu, “Proving non-termination,” *Proceedings of the 35th Annual Symposium on Principles of Programming Languages (POPL)*, ACM Press, 2008, pp. 147–158.
22. Dirk Beyer, Arindam Chakrabarti, Thomas A. Henzinger, and Sanjit A. Seshia, “An application of web-service interfaces,” *Proceedings of the International Conference on Web Services (ICWS)*, IEEE Computer Society Press, 2007, pp. 831–838.
23. Krishnendu Chatterjee, Thomas A. Henzinger, and Nir Piterman, “Strategy logic,” *Proceedings of the 18th International Conference on Concurrency Theory (CONCUR)*, Lecture Notes in Computer Science 4703, Springer, 2007, pp. 59–73. Invited to a special issue of *Information and Computation* for selected papers of CONCUR 07.
24. Thomas Brihaye, Thomas A. Henzinger, Vinayak S. Prabhu, and Jean-François Raskin, “Minimum-time reachability in timed games,” *Proceedings of the 34th International Colloquium on Automata, Languages, and Programming (ICALP)*, Lecture Notes in Computer Science 4596, Springer, 2007, pp. 825–837.

25. Dirk Beyer, Thomas A. Henzinger, and Grégory Théoduloz, “Configurable software verification: Concretizing the convergence of model checking and program analysis,” *Proceedings of the 19th International Conference on Computer-Aided Verification (CAV)*, Lecture Notes in Computer Science 4590, Springer, 2007, pp. 509–523. Invited to a special issue of *Formal Methods in System Design* for selected papers of CAV 07.
26. Dirk Beyer, Thomas A. Henzinger, Rupak Majumdar, and Andrey Rybalchenko, “Path invariants,” *Proceedings of the International Conference on Programming Language Design and Implementation (PLDI)*, ACM Press, 2007, pp. 300–309.
27. Krishnendu Chatterjee and Thomas A. Henzinger, “Assume-guarantee synthesis,” *Proceedings of the 13th International Conference on Tools and Algorithms for the Construction and Analysis of Systems (TACAS)*, Lecture Notes in Computer Science 4424, Springer, 2007, pp. 261–275.
28. Krishnendu Chatterjee, Thomas A. Henzinger, and Nir Piterman, “Generalized parity games,” *Proceedings of the 10th International Conference on Foundations of Software Science and Computation Structures (FOSSACS)*, Lecture Notes in Computer Science 4423, Springer, 2007, pp. 153–167.
29. Dirk Beyer, Thomas A. Henzinger, Rupak Majumdar, and Andrey Rybalchenko, “Invariant synthesis for combined theories,” *Proceedings of the 8th International Conference on Verification, Model Checking, and Abstract Interpretation (VMCAI)*, Lecture Notes in Computer Science 4349, Springer, 2007, pp. 378–394.
30. Bhargav Gulavani, Thomas A. Henzinger, Yamini Kannan, Aditya Nori, and Sriram K. Rajamani, “SYNERGY: A new algorithm for property checking,” *Proceedings of the 14th Annual Symposium on Foundations of Software Engineering (FSE)*, ACM Press, 2006, pp. 117–127. Best paper award at FSE 06. Invited to a special issue of *IEEE Transactions on Software Engineering* for selected papers of FSE 06.
31. Arkadeb Ghosal, Thomas A. Henzinger, Daniel Iercan, Christoph M. Kirsch, and Alberto Sangiovanni-Vincentelli, “A hierarchical coordination language for interacting real-time tasks,” *Proceedings of the Sixth Annual Conference on Embedded Software (EMSOFT)*, ACM Press, 2006, pp. 132–141.
32. Krishnendu Chatterjee, Laurent Doyen, Thomas A. Henzinger, and Jean-François Raskin, “Algorithms for omega-regular games with imperfect information,” *Proceedings of the 15th International Conference on Computer Science Logic (CSL)*, Lecture Notes in Computer Science 4207, Springer, 2006, pp. 287–302. Invited to a special issue of *Logical Methods in Computer Science* for selected papers of CSL 06.
33. Thomas A. Henzinger and Nir Piterman, “Solving games without determinization,” *Proceedings of the 15th International Conference on Computer Science Logic (CSL)*, Lecture Notes in Computer Science 4207, Springer, 2006, pp. 395–410.
34. Krishnendu Chatterjee, Luca de Alfaro, Marco Faella, Thomas A. Henzinger, Rupak Majumdar, and Marielle Stoelinga, “Compositional quantitative reasoning,” *Proceedings of the Third Annual Conference on Quantitative Evaluation of Systems (QEST)*, IEEE Computer Society Press, 2006, pp. 179–188.
35. Krishnendu Chatterjee, Luca de Alfaro, and Thomas A. Henzinger, “Strategy improvement for concurrent reachability games,” *Proceedings of the Third Annual Conference on Quantitative Evaluation of Systems (QEST)*, IEEE Computer Society Press, 2006, pp. 291–300.
36. Martin de Wulf, Laurent Doyen, Thomas A. Henzinger, and Jean-François Raskin, “Antichains: A new algorithm for checking universality of finite automata,” *Proceedings of the 18th International Conference on Computer-Aided Verification (CAV)*, Lecture Notes in Computer Science 4144, Springer, 2006, pp. 17–30.
37. Dirk Beyer, Thomas A. Henzinger, and Grégory Théoduloz, “Lazy shape analysis,” *Proceedings of the 18th International Conference on Computer-Aided Verification (CAV)*, Lecture Notes in Computer Science 4144, Springer, 2006, pp. 532–546.

38. Krishnendu Chatterjee and Thomas A. Henzinger, “Strategy improvement for stochastic Rabin and Streett games,” *Proceedings of the 17th International Conference on Concurrency Theory (CONCUR)*, Lecture Notes in Computer Science 4137, Springer, 2006, pp. 375–389.
39. Krishnendu Chatterjee and Thomas A. Henzinger, “Finitary winning in omega-regular games,” *Proceedings of the 12th International Conference on Tools and Algorithms for the Construction and Analysis of Systems (TACAS)*, Lecture Notes in Computer Science 3920, Springer, 2006, pp. 257–271.
40. Thomas A. Henzinger and Slobodan Matic, “An interface algebra for real-time components,” *Proceedings of the 12th Annual Real-Time and Embedded Technology and Applications Symposium (RTAS)*, IEEE Computer Society Press, 2006, pp. 253–266.
41. Krishnendu Chatterjee and Thomas A. Henzinger, “Strategy improvement and randomized subexponential algorithms for stochastic parity games,” *Proceedings of the 23rd International Conference on Theoretical Aspects of Computer Science (STACS)*, Lecture Notes in Computer Science 3884, Springer, 2006, pp. 512–523.
42. Krishnendu Chatterjee, Rupak Majumdar, and Thomas A. Henzinger, “Markov decision processes with multiple objectives,” *Proceedings of the 23rd International Conference on Theoretical Aspects of Computer Science (STACS)*, Lecture Notes in Computer Science 3884, Springer, 2006, pp. 325–336.
43. Krishnendu Chatterjee, Luca de Alfaro, and Thomas A. Henzinger, “The complexity of quantitative concurrent parity games,” *Proceedings of the 17th Annual Symposium on Discrete Algorithms (SODA)*, ACM Press, 2006, pp. 678–687.
44. Slobodan Matic and Thomas A. Henzinger, “Trading end-to-end latency for composability,” *Proceedings of the 26th Annual Real-Time Systems Symposium (RTSS)*, IEEE Computer Society Press, 2005, pp. 99–110.
45. Laurent Doyen, Thomas A. Henzinger, and Jean-François Raskin, “Automatic rectangular refinement of affine hybrid systems,” *Proceedings of the Third International Conference on Formal Modeling and Analysis of Timed Systems (FORMATS)*, Lecture Notes in Computer Science 3829, Springer, 2005, pp. 144–161.
46. Thomas A. Henzinger, Rupak Majumdar, and Vinayak S. Prabhu, “Quantifying similarities between timed systems,” *Proceedings of the Third International Conference on Formal Modeling and Analysis of Timed Systems (FORMATS)*, Lecture Notes in Computer Science 3829, Springer, 2005, pp. 226–241.
47. Arindam Chakrabarti, Krishnendu Chatterjee, Thomas A. Henzinger, Orna Kupferman, and Rupak Majumdar, “Verifying quantitative properties using bound functions,” *Proceedings of the 13th International Conference on Correct Hardware Design and Verification Methods (CHARME)*, Lecture Notes in Computer Science 3725, Springer, 2005, pp. 50–64.
48. Thomas A. Henzinger, Ranjit Jhala, and Rupak Majumdar, “Permissive interfaces,” *Proceedings of the 13th Annual Symposium on Foundations of Software Engineering (FSE)*, ACM Press, 2005, pp. 31–40. Invited to a special issue of *ACM Transactions on Software Engineering* for selected papers of FSE 05.
49. Krishnendu Chatterjee, Thomas A. Henzinger, Ranjit Jhala, and Rupak Majumdar, “Counterexample-guided planning,” *Proceedings of the 21st International Conference on Uncertainty in Artificial Intelligence (UAI)*, AUAI Press, 2005, pp. 104–111.
50. Krishnendu Chatterjee, Luca de Alfaro, and Thomas A. Henzinger, “The complexity of stochastic Rabin and Streett games,” *Proceedings of the 32nd International Colloquium on Automata, Languages, and Programming (ICALP)*, Lecture Notes in Computer Science 3580, Springer, 2005, pp. 878–890.
51. Krishnendu Chatterjee, Thomas A. Henzinger, and Marcin Jurdziński, “Mean-payoff parity games,” *Proceedings of the 20th Annual Symposium on Logic in Computer Science (LICS)*, IEEE Computer Society Press, 2005, pp. 178–187.

52. Thomas A. Henzinger, Christoph M. Kirsch, and Slobodan Matic, “Composable code generation for distributed GIOTTO,” *Proceedings of the International Conference on Languages, Compilers, and Tools for Embedded Systems (LCTES)*, ACM Press, 2005, pp. 21–30.
53. Christoph M. Kirsch, Marco A.A. Sanvido, and Thomas A. Henzinger, “A programmable microkernel for real-time systems,” *Proceedings of the First International Conference on Virtual Execution Environments (VEE)*, ACM Press, 2005, pp. 35–45.
54. Dirk Beyer, Arindam Chakrabarti, and Thomas A. Henzinger, “Web service interfaces,” *Proceedings of the 14th International World-Wide Web Conference (WWW)*, 2005, pp. 148–159.
55. Thomas A. Henzinger and Christoph M. Kirsch, “A typed assembly language for real-time programs,” *Proceedings of the Fourth Annual Conference on Embedded Software (EMSOFT)*, ACM Press, 2004, pp. 104–113.
56. Krishnendu Chatterjee, Thomas A. Henzinger, and Marcin Jurdziński, “Games with secure equilibria,” *Proceedings of the 19th Annual Symposium on Logic in Computer Science (LICS)*, IEEE Computer Society Press, 2004, pp. 160–169.
57. Thomas A. Henzinger, Ranjit Jhala, and Rupak Majumdar, “Race checking by context inference,” *Proceedings of the International Conference on Programming Language Design and Implementation (PLDI)*, ACM Press, 2004, pp. 1–13.
58. Dirk Beyer, Thomas A. Henzinger, Ranjit Jhala, and Rupak Majumdar, “An Eclipse plug-in for model checking,” *Proceedings of the 12th Annual International Workshop on Program Comprehension (IWPC)*, IEEE Computer Society Press, 2004, pp. 251–257.
59. Dirk Beyer, Adam Chlipala, Thomas A. Henzinger, Ranjit Jhala, and Rupak Majumdar, “Generating tests from counterexamples,” *Proceedings of the 26th Annual International Conference on Software Engineering (ICSE)*, IEEE Computer Society Press, 2004, pp. 326–335.
60. Arkadeb Ghosal, Thomas A. Henzinger, Christoph M. Kirsch, and Marco A.A. Sanvido, “Event-driven programming with logical execution times,” *Proceedings of the Seventh International Workshop on Hybrid Systems: Computation and Control (HSCC)*, Lecture Notes in Computer Science 2993, Springer, 2004, pp. 357–371. Invited to a special issue of *Formal Methods in System Design* for selected papers of HSCC 04.
61. Luca de Alfaro, Marco Faella, Thomas A. Henzinger, Rupak Majumdar, and Marielle Stoelinga, “Model checking discounted temporal properties,” *Proceedings of the 10th International Conference on Tools and Algorithms for the Construction and Analysis of Systems (TACAS)*, Lecture Notes in Computer Science 2988, Springer, 2004, pp. 77–92. Invited to a special issue of *Theoretical Computer Science* for selected papers of TACAS 04.
62. Thomas A. Henzinger, Ranjit Jhala, Rupak Majumdar, and Kenneth L. McMillan, “Abstractions from proofs,” *Proceedings of the 31st Annual Symposium on Principles of Programming Languages (POPL)*, ACM Press, 2004, pp. 232–244.
63. Krishnendu Chatterjee, Marcin Jurdziński, and Thomas A. Henzinger, “Quantitative stochastic parity games,” *Proceedings of the 15th Annual Symposium on Discrete Algorithms (SODA)*, ACM Press, 2004, pp. 114–123.
64. Thomas A. Henzinger, Christoph M. Kirsch, and Slobodan Matic, “Schedule-carrying code,” *Proceedings of the Third Annual Conference on Embedded Software (EMSOFT)*, Lecture Notes in Computer Science 2855, Springer, 2003, pp. 241–256. Invited to a special issue of the *ACM Transactions on Embedded Systems* for selected papers of EMSOFT 03.
65. Arindam Chakrabarti, Luca de Alfaro, Thomas A. Henzinger, and Marielle Stoelinga, “Resource interfaces,” *Proceedings of the Third Annual Conference on Embedded Software (EMSOFT)*, Lecture Notes in Computer Science 2855, Springer, 2003, pp. 117–133.

66. Krishnendu Chatterjee, Marcin Jurdziński, and Thomas A. Henzinger, “Simple stochastic parity games,” *Proceedings of the 12th International Conference on Computer Science Logic (CSL)*, Lecture Notes in Computer Science 2803, Springer, 2003, pp. 100–113.
67. Luca de Alfaro, Marco Faella, Thomas A. Henzinger, Rupak Majumdar, and Marielle Stoelinga, “The element of surprise in timed games,” *Proceedings of the 14th International Conference on Concurrency Theory (CONCUR)*, Lecture Notes in Computer Science 2761, Springer, 2003, pp. 144–158.
68. Thomas A. Henzinger, Ranjit Jhala, Rupak Majumdar, and Shaz Qadeer, “Thread-modular abstraction refinement,” *Proceedings of the 15th International Conference on Computer-Aided Verification (CAV)*, Lecture Notes in Computer Science 2725, Springer, 2003, pp. 262–274.
69. Thomas A. Henzinger, Ranjit Jhala, and Rupak Majumdar, “Counterexample-guided control,” *Proceedings of the 30th International Colloquium on Automata, Languages, and Programming (ICALP)*, Lecture Notes in Computer Science 2719, Springer, 2003, pp. 886–902.
70. Luca de Alfaro, Thomas A. Henzinger, and Rupak Majumdar, “Discounting the future in systems theory,” *Proceedings of the 30th International Colloquium on Automata, Languages, and Programming (ICALP)*, Lecture Notes in Computer Science 2719, Springer, 2003, pp. 1022–1037.
71. Krishnendu Chatterjee, Di Ma, Rupak Majumdar, Tian Zhao, Thomas A. Henzinger, and Jens Palmberg, “Stack-size analysis for interrupt-driven programs,” *Proceedings of the Tenth International Static Analysis Symposium (SAS)*, Lecture Notes in Computer Science 2694, Springer, 2003, pp. 109–126. Invited to a special issue of *Science of Computer Programming* for selected papers of SAS 03.
72. Thomas A. Henzinger, Ranjit Jhala, Rupak Majumdar, and Grégoire Sutre, “Software verification with BLAST,” *Proceedings of the Tenth International Workshop on Model Checking of Software (SPIN)*, Lecture Notes in Computer Science 2648, Springer, 2003, pp. 235–239.
73. Thomas A. Henzinger, Orna Kupferman, and Rupak Majumdar, “On the universal and existential fragments of the mu-calculus,” *Proceedings of the Ninth International Conference on Tools and Algorithms for the Construction and Analysis of Systems (TACAS)*, Lecture Notes in Computer Science 2619, Springer, 2003, pp. 49–64. Invited to a special issue of *Theoretical Computer Science* for selected papers of TACAS 03.
74. Roberto Passerone, Luca de Alfaro, Thomas A. Henzinger, and Alberto L. Sangiovanni-Vincentelli, “Convertibility verification and converter synthesis: Two faces of the same coin,” *Proceedings of the International Conference on Computer-Aided Design (ICCAD)*, IEEE Computer Society Press, 2002, pp. 132–139.
75. Thomas A. Henzinger, Christoph M. Kirsch, Rupak Majumdar, and Slobodan Matic, “Time-safety checking for embedded programs,” *Proceedings of the Second International Workshop on Embedded Software (EMSOFT)*, Lecture Notes in Computer Science 2491, Springer, 2002, pp. 76–92.
76. Luca de Alfaro, Thomas A. Henzinger, and Mariëlle Stoelinga, “Timed interfaces,” *Proceedings of the Second International Workshop on Embedded Software (EMSOFT)*, Lecture Notes in Computer Science 2491, Springer, 2002, pp. 108–122.
77. Christoph M. Kirsch, Marco A. Sanvido, Thomas A. Henzinger, and Wolfgang Pree, “A GIOTTO-based helicopter control system,” *Proceedings of the Second International Workshop on Embedded Software (EMSOFT)*, Lecture Notes in Computer Science 2491, Springer, 2002, pp. 46–60.
78. Marcin Jurdziński, Orna Kupferman, and Thomas A. Henzinger, “Trading probability for fairness,” *Proceedings of the 11th International Conference on Computer Science Logic (CSL)*, Lecture Notes in Computer Science 2471, Springer, 2002, pp. 292–305.
79. Arindam Chakrabarti, Luca de Alfaro, Thomas A. Henzinger, and Freddy Y.C. Mang, “Synchronous and bidirectional component interfaces,” *Proceedings of the 14th International Conference on Computer-Aided Verification (CAV)*, Lecture Notes in Computer Science 2404, Springer, 2002, pp. 414–427.

80. Arindam Chakrabarti, Luca de Alfaro, Thomas A. Henzinger, Marcin Jurdziński, and Freddy Y.C. Mang, “Interface compatibility checking for software modules,” *Proceedings of the 14th International Conference on Computer-Aided Verification (CAV)*, Lecture Notes in Computer Science 2404, Springer, 2002, pp. 428–441.
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Refereed Journal Papers

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2. Thomas A. Henzinger, “Two challenges in embedded systems design: Predictability and robustness,” *Philosophical Transactions of the Royal Society A* 366:3727–3736, 2008. Special issue on *Ubiquitous Computing*; invited contribution.
3. Krishnendu Chatterjee, Rupak Majumdar, and Thomas A. Henzinger, “Stochastic limit-average games are in EXPTIME,” *International Journal of Game Theory* 37:219–234, 2008.
4. Laurent Doyen, Thomas A. Henzinger, and Jean-François Raskin, “Equivalence of labeled Markov chains,” *International Journal of Foundations of Computer Science* 19:549–563, 2008. Special issue for selected papers of DLT 07; invited contribution.
5. Krishnendu Chatterjee and Thomas A. Henzinger, “Reduction of stochastic parity to stochastic mean-payoff games,” *Information Processing Letters* 106:1–7, 2008.
6. Jasmin Fisher and Thomas A. Henzinger, “Executable cell biology,” *Nature Biotechnology* 25:1239–1249, 2007. See also correspondence in *Nature Biotechnology* 26:737–9, 2008.
7. Thomas A. Henzinger and Joseph Sifakis, “The discipline of embedded systems design,” *IEEE Computer* 40(10):36–44, 2007. Cover feature; invited contribution.
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11. Dirk Beyer, Thomas A. Henzinger, Ranjit Jhala, and Rupak Majumdar, “The software model checker BLAST: Applications to software engineering,” *Software Tools for Technology Transfer* 9:505–526, 2007. Special issue for selected papers of FASE 05; invited contribution.
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15. Thomas A. Henzinger, Orna Kupferman, and Rupak Majumdar, “On the universal and existential fragments of the mu-calculus,” *Theoretical Computer Science* 354:173–186, 2006. Special issue for selected papers of TACAS 03; invited contribution.
16. Luca de Alfaro, Marco Faella, Thomas A. Henzinger, Rupak Majumdar, and Marielle Stoelinga, “Model checking discounted temporal properties,” *Theoretical Computer Science* 345:139–170, 2005. Special issue for selected papers of TACAS 04; invited contribution.
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19. Thomas A. Henzinger, Orna Kupferman, and Shaz Qadeer, “From prehistoric to postmodern symbolic model checking,” *Formal Methods in System Design* 23:303–327, 2003.
20. Thomas A. Henzinger, Benjamin Horowitz, and Christoph M. Kirsch, “GIOTTO: A time-triggered language for embedded programming,” *Proceedings of the IEEE* 91:84–99, 2003. Special issue on *Modeling and Design of Embedded Software*; invited contribution.
21. Thomas A. Henzinger, Christoph M. Kirsch, Marco A. Sanvido, and Wolfgang Pree, “From control models to real-time code using GIOTTO,” *IEEE Control Systems Magazine* 23(1):50–64, 2003. Special issue on *Software-Enabled Control*; invited contribution.
22. Rajeev Alur, Thomas A. Henzinger, and Orna Kupferman, “Alternating-time temporal logic,” *Journal of the ACM* 49:672–713, 2002.
23. Thomas A. Henzinger, Shaz Qadeer, Sriram K. Rajamani, and Serdar Tasiran, “An assume-guarantee rule for checking simulation,” *ACM Transactions on Programming Languages and Systems* 24:51–64, 2002.
24. Thomas A. Henzinger, Orna Kupferman, and Sriram K. Rajamani, “Fair simulation,” *Information and Computation* 173:64–81, 2002.
25. Jean-François Raskin, Pierre-Yves Schobbens, and Thomas A. Henzinger, “Axioms for real-time logics,” *Theoretical Computer Science* 274:151–182, 2002. Special issue for selected papers of CONCUR 98; invited contribution.

26. Rajeev Alur, Robert K. Brayton, Thomas A. Henzinger, Shaz Qadeer, and Sriram K. Rajamani, "Partial-order reduction in symbolic state-space exploration," *Formal Methods in System Design* 18:97–116, 2001. Special issue for selected papers of CAV 97; invited contribution.
27. Rajeev Alur, Thomas A. Henzinger, Gerardo Lafferriere, and George J. Pappas, "Discrete abstractions of hybrid systems," *Proceedings of the IEEE* 88:971–984, 2000. Special issue on *Hybrid Systems*; invited contribution.
28. Thomas A. Henzinger and Peter W. Kopke, "Discrete-time control for rectangular hybrid automata," *Theoretical Computer Science* 221:369–392, 1999. Special issue for selected papers of ICALP 97; invited contribution.
29. Rajeev Alur and Thomas A. Henzinger, "Reactive modules," *Formal Methods in System Design* 15:7–48, 1999. Special issue for selected papers of LICS 96; invited contribution.
30. Rajeev Alur, Limor Fix, and Thomas A. Henzinger, "Event-clock automata: A determinizable class of timed automata," *Theoretical Computer Science* 211:253–273, 1999.
31. Thomas A. Henzinger, Pei-Hsin Ho, and Howard Wong-Toi, "Algorithmic analysis of nonlinear hybrid systems," *IEEE Transactions on Automatic Control* 43:540–554, 1998. Special issue on *Hybrid Systems*; invited contribution.
32. Thomas A. Henzinger, Peter W. Kopke, Anuj Puri, and Pravin Varaiya, "What's decidable about hybrid automata?," *Journal of Computer and System Sciences* 57:94–124, 1998. Special issue for selected papers of STOC 95; invited contribution.
33. Rajeev Alur and Thomas A. Henzinger, "Finitary fairness," *ACM Transactions on Programming Languages and Systems* 20:1171–1194, 1998.
34. Thomas A. Henzinger, Pei-Hsin Ho, and Howard Wong-Toi, "HYTECH: A model checker for hybrid systems," *Software Tools for Technology Transfer* 1:110–122, 1997. Special issue on *Timed and Hybrid Systems*; invited contribution.
35. Rajeev Alur and Thomas A. Henzinger, "Real-time system = discrete system + clock variables," *Software Tools for Technology Transfer* 1:86–109, 1997. Special issue on *Timed and Hybrid Systems*; invited contribution.
36. Rajeev Alur, Costas Courcoubetis, and Thomas A. Henzinger, "Computing accumulated delays in real-time systems," *Formal Methods in System Design* 11:137–156, 1997. Special issue for selected papers of CAV 93; invited contribution.
37. Rajeev Alur, Thomas A. Henzinger, and Pei-Hsin Ho, "Automatic symbolic verification of embedded systems," *IEEE Transactions on Software Engineering* 22:181–201, 1996.
38. Rajeev Alur, Tomás Feder, and Thomas A. Henzinger, "The benefits of relaxing punctuality," *Journal of the ACM* 43:116–146, 1996.
39. Rajeev Alur, Costas Courcoubetis, Nicolas Halbwachs, Thomas A. Henzinger, Pei-Hsin Ho, Xavier Nicollin, Alfredo Olivero, Joseph Sifakis, and Sergio Yovine, "The algorithmic analysis of hybrid systems," *Theoretical Computer Science* 138:3–34, 1995. Special issue on *Hybrid Systems*; invited contribution.
40. Thomas A. Henzinger, Zohar Manna, and Amir Pnueli, "Temporal proof methodologies for timed transition systems," *Information and Computation* 112:273–337, 1994.
41. Thomas A. Henzinger, Xavier Nicollin, Joseph Sifakis, and Sergio Yovine, "Symbolic model checking for real-time systems," *Information and Computation* 111:193–244, 1994. Special issue for selected papers of LICS 92; invited contribution.

42. Rajeev Alur and Thomas A. Henzinger, “A really temporal logic,” *Journal of the ACM* 41:181–204, 1994.
43. Rajeev Alur and Thomas A. Henzinger, “Real-time logics: Complexity and expressiveness,” *Information and Computation* 104:35–77, 1993. Special issue for selected papers of LICS 90; invited contribution.
44. Thomas A. Henzinger, “Sooner is safer than later,” *Information Processing Letters* 43:135–141, 1992.

Invited Papers

1. Krishnendu Chatterjee and Thomas A. Henzinger, “Value iteration,” in *25 Years of Model Checking*, Lecture Notes in Computer Science 5000, Springer, 2008, pp. 107–138.
2. Thomas A. Henzinger, “Quantitative generalizations of languages,” *Proceedings of the 11th International Conference on Developments in Language Theory (DLT)*, Lecture Notes in Computer Science 4588, Springer, 2007, pp. 20–22. Keynote lecture at DLT 07.
3. Dirk Beyer, Thomas A. Henzinger, and Vasu Singh, “Algorithms for interface synthesis,” *Proceedings of the 19th International Conference on Computer-Aided Verification (CAV)*, Lecture Notes in Computer Science 4590, Springer, 2007, pp. 4–19. Invited tutorial at CAV 07.
4. Roman Manevich, John Field, Thomas A. Henzinger, Ganesan Ramalingam, and Mooly Sagiv, “Abstract counterexample-based refinement for powerset domains,” in *Program Analysis and Compilation: Theory and Practice*, Lecture Notes in Computer Science 4444, Springer, 2007, pp. 273–292. Invited contribution.
5. Thomas A. Henzinger, “Games, time, and probability: Graph models for system design and analysis,” *Proceedings of the 33rd International Conference on Current Trends in Theory and Practice of Computer Science (SOFSEM)*, Lecture Notes in Computer Science 4362, Springer, 2007, pp. 103–110. Invited lecture at SOFSEM 07.
6. Jasmin Fisher and Thomas A. Henzinger, “Executable biology,” *Proceedings of the Winter Simulation Conference (WSC)*, IEEE Computer Society Press, 2006, pp. 1675–1682. Invited lecture at WSC 06.
7. Thomas A. Henzinger and Vinayak S. Prabhu, “Timed alternating-time temporal logic,” *Proceedings of the Fourth International Conference on Formal Modeling and Analysis of Timed Systems (FORMATS)*, Lecture Notes in Computer Science 4202, Springer, 2006, pp. 1–17. Keynote lecture at FORMATS 06.
8. Thomas A. Henzinger and Joseph Sifakis, “The embedded systems design challenge,” *Proceedings of the 14th International Symposium on Formal Methods (FM)*, Lecture Notes in Computer Science 4085, Springer, 2006, pp. 1–15. Keynote lecture at FM 06.
9. Krishnendu Chatterjee and Thomas A. Henzinger, “Semiperfect-information games,” *Proceedings of the 25th Annual Conference on Foundations of Software Technology and Theoretical Computer Science (FSTTCS)*, Lecture Notes in Computer Science 3821, Springer, 2005, pp. 1–18. Keynote lecture at FSTTCS 05.
10. Luca de Alfaro and Thomas A. Henzinger, “Interface-based Design,” in *Engineering Theories of Software-intensive Systems* (M. Broy, J. Grünbauer, D. Harel, and C.A.R. Hoare, eds.), NATO Science Series: Mathematics, Physics, and Chemistry, Vol. 195, Springer, 2005, pp. 83–104. Invited lecture series at the NATO International Summer School on Engineering Theories for Software-intensive Systems, Marktoberdorf, Germany, August 2004.
11. Thomas A. Henzinger, Ranjit Jhala, and Rupak Majumdar, “The BLAST software verification system,” *Proceedings of the 12th International Workshop on Model Checking of Software (SPIN)*, Lecture Notes in Computer Science 3639, Springer, 2005, pp. 25–26. Invited tutorial at SPIN 05.

12. Thomas A. Henzinger, “Games in system design and verification,” *Proceedings of the Tenth International Conference on Theoretical Aspects of Rationality and Knowledge (TARK)*, 2005, pp. 1–4. Keynote lecture at TARK 05.
13. Dirk Beyer, Thomas A. Henzinger, Ranjit Jhala, and Rupak Majumdar, “Checking memory safety with BLAST,” *Proceedings of the International Conference on Fundamental Approaches to Software Engineering (FASE)*, Lecture Notes in Computer Science 3442, Springer, 2005, pp. 2–18. Keynote lecture at FASE 05. Invited to a special issue of *Software Tools for Technology Transfer* for selected papers of FASE 05.
14. Krishnendu Chatterjee, Luca de Alfaro, and Thomas A. Henzinger, “Trading memory for randomness,” *Proceedings of the First Annual Conference on Quantitative Evaluation of Systems (QEST)*, IEEE Computer Society Press, 2004, pp. 206–217. Keynote lecture at QEST 04.
15. Dirk Beyer, Adam Chlipala, Thomas A. Henzinger, Ranjit Jhala, and Rupak Majumdar, “The BLAST query language for software verification,” *Proceedings of the 11th International Static Analysis Symposium (SAS)*, Lecture Notes in Computer Science 3148, Springer, 2004, pp. 2–18. Keynote lecture at SAS 04.
16. Thomas A. Henzinger, “Embedded software: Better models, better code,” *Proceedings of the 25th International Conference on Applications and Theory of Petri Nets (ATPN)*, Lecture Notes in Computer Science 3099, Springer, 2004, pp. 35–36. Keynote lecture at ATPN 04.
17. Thomas A. Henzinger, “Rich interfaces for software modules,” *Proceedings of the 18th European Conference on Object-Oriented Programming (ECOOP)*, Lecture Notes in Computer Science 3086, Springer, 2004, pp. 517–518. Keynote lecture at ECOOP 04.
18. Thomas A. Henzinger, Ranjit Jhala, Rupak Majumdar, and Marco A.A. Sanvido, “Extreme model checking,” in *Verification: Theory and Practice*, Lecture Notes in Computer Science 2772, Springer, 2004, pp. 332–358. Invited contribution.
19. Thomas A. Henzinger, “Model checking: From hardware to software,” *Proceedings of the First Asian Symposium on Programming Languages and Systems (APLAS)*, Lecture Notes in Computer Science 2895, Springer, 2003, pp. 176–177. Keynote lecture at APLAS 03.
20. Thomas A. Henzinger, “Automata for specifying component interfaces,” *Proceedings of the Eighth International Conference on Implementation and Application of Automata (CIAA)*, Lecture Notes in Computer Science 2759, Springer, 2003, pp. 1–2. Keynote lecture at CIAA 03.
21. Thomas A. Henzinger, Benjamin Horowitz, and Christoph M. Kirsch, “Embedded control systems development with GIOTTO,” in *Software-Enabled Control: Information Technology for Dynamical Systems* (T. Samad and G. Balas, eds.), IEEE Press and Wiley-Interscience, 2003, pp. 123–146. Invited contribution.
22. Thomas A. Henzinger, “The symbolic approach to hybrid systems,” *Proceedings of the 14th International Conference on Computer-Aided Verification (CAV)*, Lecture Notes in Computer Science 2404, Springer, 2002, page 57. Invited tutorial at CAV 02.
23. Thomas A. Henzinger, “From models to code: The missing link in embedded software,” *Proceedings of the Fifth International Workshop on Hybrid Systems: Computation and Control (HSCC)*, Lecture Notes in Computer Science 2289, Springer, 2002, pp. 5–6. Keynote lecture at HSCC 02.
24. Thomas A. Henzinger, Benjamin Horowitz, and Christoph M. Kirsch, “GIOTTO: A time-triggered language for embedded programming,” *Proceedings of the First International Workshop on Embedded Software (EMSOFT)*, Lecture Notes in Computer Science 2211, Springer, 2001, pp. 166–184. Invited lecture at EMSOFT 01.

25. Luca de Alfaro and Thomas A. Henzinger, “Interface theories for component-based design,” *Proceedings of the First International Workshop on Embedded Software (EMSOFT)*, Lecture Notes in Computer Science 2211, Springer, 2001, pp. 148–165. Invited lecture at EMSOFT 01.
26. Alberto L. Sangiovanni-Vincentelli, Thomas A. Henzinger, Bruce H. Krogh, Oded Maler, Manfred Morari, Costas C. Pantelides, George J. Pappas, Tunc Simsec, Janos Sztipanovits, and Stavros Tripakis, “Hybrid systems applications: An oxymoron?,” *Proceedings of the Fifth International Workshop on Hybrid Systems: Computation and Control (HSCC)*, Lecture Notes in Computer Science 2034, Springer, 2001, pp. 5–6. Panel statement at HSCC 01.
27. Thomas A. Henzinger, Shaz Qadeer, and Sriram K. Rajamani, “Decomposing refinement proofs using assume-guarantee reasoning,” *Proceedings of the International Conference on Computer-Aided Design (ICCAD)*, IEEE Computer Society Press, 2000, pp. 245–252. Invited tutorial at ICCAD 00.
28. Thomas A. Henzinger, “MASACCIO: A formal model for embedded components,” *Proceedings of the First IFIP International Conference on Theoretical Computer Science (TCS)*, Lecture Notes in Computer Science 1872, Springer, 2000, pp. 549–563. Invited lecture at TCS 00.
29. Thomas A. Henzinger and Rupak Majumdar, “A classification of symbolic transition systems,” *Proceedings of the 17th International Conference on Theoretical Aspects of Computer Science (STACS)*, Lecture Notes in Computer Science 1770, Springer, 2000, pp. 13–34. Keynote lecture at STACS 00.
30. Thomas A. Henzinger, “The theory of hybrid automata,” in *Verification of Digital and Hybrid Systems* (M.K. Inan and R.P. Kurshan, eds.), NATO ASI Series F: Computer and Systems Sciences, Vol. 170, Springer, 2000, pp. 265–292. Invited lecture series at the NATO-ASI International Summer School on the Verification of Digital and Hybrid Systems, Antalya, Turkey, June 1997.
31. Thomas A. Henzinger, “It’s about time: Real-time logics reviewed,” *Proceedings of the Ninth International Conference on Concurrency Theory (CONCUR)*, Lecture Notes in Computer Science 1466, Springer, 1998, pp. 439–454. Keynote lecture at CONCUR 98.
32. Rajeev Alur, Thomas A. Henzinger, and Orna Kupferman, “Alternating-time temporal logic,” in *Compositionality: The Significant Difference* (W.-P. de Roever, H. Langmaack, and A. Pnueli, eds.), Lecture Notes in Computer Science 1536, Springer, 1998, pp. 23–60. Invited lecture at the International Symposium on Compositionality, Bad Malente-Gremsmühlen, Germany, September 1997.
33. Thomas A. Henzinger, “Model checking game properties of multi-agent systems,” *Proceedings of the 25th International Colloquium on Automata, Languages, and Programming (ICALP)*, Lecture Notes in Computer Science 1443, Springer, 1998, page 543. Keynote lecture at ICALP 98.
34. Thomas A. Henzinger, “The theory of hybrid automata,” *Proceedings of the 11th Annual Symposium on Logic in Computer Science (LICS)*, IEEE Computer Society Press, 1996, pp. 278–292. Invited tutorial at LICS 96.
35. Thomas A. Henzinger, “Some myths about formal verification,” *ACM Computing Surveys* 28(4), 1996, page 119. Invited contribution.
36. Rajeev Alur and Thomas A. Henzinger, “Real-time system = discrete system + clock variables,” in *Theories and Experiences for Real-Time System Development* (T. Rus and C. Rattray, eds.), AMAST Series in Computing Vol. 2, World Scientific, 1994, pp. 1–29. Invited lecture at the First AMAST Workshop on Real-Time Systems (ARTS), Iowa City, Iowa, November 1993.
37. Thomas A. Henzinger, Zohar Manna, and Amir Pnueli, “Timed transition systems,” in *Real Time: Theory in Practice* (J.W. de Bakker, C. Huizing, W.-P. de Roever, and G. Rozenberg, eds.), Lecture Notes in Computer Science 600, Springer, 1992, pp. 226–251. Invited lecture at the REX Workshop on Real-Time Systems (REX), Mook, The Netherlands, June 1991.

38. Rajeev Alur and Thomas A. Henzinger, “Logics and models of real time: A survey,” in *Real Time: Theory in Practice* (J.W. de Bakker, C. Huizinga, W.-P. de Roever, and G. Rozenberg, eds.), Lecture Notes in Computer Science 600, Springer, 1992, pp. 74–106. Invited contribution.
39. Rajeev Alur and Thomas A. Henzinger, “Time for logic,” *SIGACT News* 22(3), 1991, pp. 6–12. Invited logic column.

Conference Abstracts

1. Marc Schaub, April Bezdek, Thomas A. Henzinger, Freddy Radtke, and Jasmin Fisher, “Qualitative crosstalk analysis of Wnt and Notch signaling in mammalian skin,” Third Annual RECOMB Satellite Conference on Systems Biology, San Diego, California, December 2007.
2. Jasmin Fisher, Nir Piterman, Alex Hajnal, and Thomas A. Henzinger, “Computational modeling shedding light on development,” 15th Annual International Conference on Intelligent Systems for Molecular Biology (ISMB) and Sixth European Conference on Computational Biology (ECCB), Vienna, Austria, July 2007.
3. Jasmin Fisher, Nir Piterman, Alex Hajnal, and Thomas A. Henzinger, “Predictive modeling of signaling crosstalk during *C. elegans* vulval development,” 16th International *C. elegans* Meeting, Los Angeles, California, June 2007.
4. Marc Schaub, Thomas A. Henzinger, and Jasmin Fisher, “Qualitative networks: A symbolic approach to analyze biological signaling networks,” Second Annual RECOMB Satellite Conference on Systems Biology, San Diego, California, December 2006.
5. Jasmin Fisher, Nir Piterman, and Thomas A. Henzinger, “Computational modeling and analysis of *C. elegans* vulval development,” European Worm Meeting, Hersonissos, Greece, April 2006.

Editor

1. Jacques Duparc and Thomas A. Henzinger, editors, *Proceedings of the 21st International Conference on Computer Science Logic (CSL)*, Lecture Notes in Computer Science 4646, Springer, 2007.
2. Thomas A. Henzinger and Christoph M. Kirsch, editors, *Proceedings of the First International Workshop on Embedded Software (EMSOFT)*, Lecture Notes in Computer Science 2211, Springer, 2001.
3. Rajeev Alur and Thomas A. Henzinger, editors, *Information and Computation* 164(2), 2001. Special issue for selected papers of LICS 96.
4. Rajeev Alur and Thomas A. Henzinger, editors, *Formal Methods in System Design* 15(1), 1999. Special issue for selected papers of LICS 96.
5. Rajeev Alur and Thomas A. Henzinger, editors, *Formal Methods in System Design* 14(3), 1998. Special issue for selected papers of CAV 96.
6. Thomas A. Henzinger and Shankar Sastry, editors, *Proceedings of the First International Workshop on Hybrid Systems: Computation and Control (HSCC)*, Lecture Notes in Computer Science 1386, Springer, 1998.
7. Rajeev Alur and Thomas A. Henzinger, editors, *Proceedings of the Eighth International Conference on Computer-Aided Verification (CAV)*, Lecture Notes in Computer Science 1102, Springer, 1996.
8. Rajeev Alur, Thomas A. Henzinger, and Eduardo D. Sontag, editors, *Hybrid Systems III: Verification and Control*, Lecture Notes in Computer Science 1066, Springer, 1996. Proceedings of the International Workshop on Hybrid Systems, New Brunswick, New Jersey, October 1995.

Other Publications

1. Thomas A. Henzinger, Foreword for *Systems and Software Verification* by B. Berard, M. Bidoit, A. Finkel, F. Laroussinie, A. Petit, L. Petrucci, and P. Schnoebelen, Springer, 2001, page v.
2. Thomas A. Henzinger, Book Review for *Verifying Temporal Properties of Systems* by J.C. Bradfield, *SIGACT News* 24(1):27–28, 1993.
3. Edward Chang and Thomas A. Henzinger, Solution Manual for *The Deductive Foundations of Computer Programming* by Z. Manna and R. Waldinger, Addison-Wesley, 1992, 350 pages.
4. Thomas A. Henzinger, Solution Manual for *The Logical Basis for Computer Programming, Volume 2* by Z. Manna and R. Waldinger, Addison-Wesley, 1990, 293 pages.

Technical Reports

This list does not include reports whose titles coincide with publications listed in the previous categories.

1. Roderick Bloem, Karin Greimel, Thomas A. Henzinger, and Barbara Jobstmann, “Synthesizing Robust Systems,” *Proceedings of the Workshop on Games for Design, Verification, and Synthesis (GASICS)*, 2009.
2. Rachid Guerraoui, Thomas A. Henzinger, Michal Kapalka, and Vasu Singh, “Generalizing the Correctness of Transactional Memory,” *Proceedings of the Workshop on Exploiting Concurrency Efficiently and Correctly (ECEC)*, 2009.
3. Rachid Guerraoui, Thomas A. Henzinger, Barbara Jobstmann, and Vasu Singh, “Model Checking Transactional Memories,” *Proceedings of the Workshop on Exploiting Concurrency Efficiently and Correctly (ECEC)*, 2008.
4. Krishnendu Chatterjee, Luca de Alfaro, and Thomas A. Henzinger, “Strategy improvement for concurrent safety games,” *Computing Research Repository (CoRR)* abs/0804.4530, 2008.
5. Krishnendu Chatterjee and Thomas A. Henzinger, “Probabilistic systems with LimSup and LimInf objectives,” *Computing Research Repository (CoRR)* abs/0809.1465, 2008.
6. Thomas A. Henzinger and Slobodan Matic, “Interfaces for real-time components,” *Proceedings of the Sixth International Symposium on Formal Methods for Components and Objects (FMCO)*, 2007.
7. Arkadeb Ghosal, Daniel Iercan, Christoph M. Kirsch, Thomas A. Henzinger, and Alberto Sangiovanni-Vincentelli, “Separate compilation of hierarchical real-time programs into linear-bounded embedded machine code,” *Proceedings of the International Workshop on Automatic Program Generation for Embedded Systems (APGES)*, 2007.
8. Krishnendu Chatterjee, Thomas A. Henzinger, and Nir Piterman, “Algorithms for Büchi games,” *Proceedings of the Third International Workshop on Games in Design and Verification (GDV)*, 2006. Also *Computing Research Repository (CoRR)* abs/0805.2620, 2008.
9. Thomas A. Henzinger, “From graph models to game models,” *Proceedings of the 25 Years of Model Checking Celebration* (O. Grumberg and H. Veith, eds.), 2006. Invited lecture.
10. Dirk Beyer, Arindam Chakrabarti, and Thomas A. Henzinger, “An interface formalism for web services,” *Proceedings of the First International Workshop on Foundations of Interface Technologies (FIT)*, 2005.
11. Thomas A. Henzinger, “The logical execution time assumption,” *Proceedings of the Workshop on Software Engineering for Embedded Systems* (M. Dwyer, B. Krogh, and I. Lee, eds.), University of Illinois, Chicago, September 2003. Invited lecture.

12. Thomas A. Henzinger, “Logics for reasoning about real-time computation,” *Volume of Abstracts of the 11th International Congress of Logic, Methodology, and Philosophy of Science*, International Union of History and Philosophy of Science, Cracow, August 1999. Invited lecture.
13. Thomas A. Henzinger, “Reactive and hybrid systems,” in *Fourth Biennial Report* (K. Mehlhorn, ed.), Max-Planck Institute for Computer Science, Saarbrücken, June 1999. Invited contribution.
14. Thomas A. Henzinger, Benjamin Horowitz, and Rupak Majumdar, “Interval-numerical methods for hybrid-systems analysis,” *Proceedings of the AAAI Spring Symposium on Hybrid Systems and Artificial Intelligence* (G. Biswas and S. McIlraith, eds.), Technical Report SS-99-05, AAAI Press, March 1999. Keynote lecture.
15. Thomas A. Henzinger, “Computer-Aided Verification of Embedded Systems,” *Proceedings of the 15th IFIP World Computer Congress* (K. Mehlhorn, ed.), Technical Report 117, Austrian Computer Society, August 1998. Invited lecture.
16. Thomas A. Henzinger and Peter W. Kopke, *Hybrid Automata with Finite Mutual Simulations*, Technical Report CSD-TR-95-1497, Cornell University, March 1995.
17. Thomas A. Henzinger and Peter W. Kopke, *Undecidability Results for Hybrid Systems*, Technical Report CSD-TR-95-1483, Cornell University, February 1995. Invited lecture at the International Workshop on Hybrid Systems and Autonomous Control, Ithaca, New York, October 1994.
18. Thomas A. Henzinger, Anuj Puri, and Pravin Varaiya, *Clock Transformation of Hybrid Automata with Rectangular Differential Inclusions*, Technical Report, University of California, Berkeley, October 1994. Invited lecture at the International Workshop on Hybrid Systems and Autonomous Control, Ithaca, New York, October 1994.
19. Thomas A. Henzinger and Pei-Hsin Ho, *Model-checking Strategies for Linear Hybrid Systems*, Technical Report CSD-TR-94-1437, Cornell University, July 1994. Invited lecture at the Seventh International Conference on Industrial and Engineering Applications of Artificial Intelligence and Expert Systems (IEA/AIE), Austin, Texas, May 1994.
20. Thomas A. Henzinger, *The Temporal Specification and Verification of Real-Time Systems*, Ph.D. Thesis (Zohar Manna, advisor), Technical Report STAN-CS-91-1380, Stanford University, August 1991. Nominated for the ACM Best Dissertation Award by Stanford University.
21. Thomas A. Henzinger, “When eventually isn’t good enough,” *Proceedings of the 23rd Annual Meeting of the Stanford Computer Forum* (J.D. Ullman and J. Hennessy, eds.), Technical Report, Stanford University, February 1991.
22. Thomas A. Henzinger, “The temporal specification and verification of real-time systems,” *Proceedings of the Berkeley Workshop on Temporal and Real-Time Specification* (P.B. Ladkin and F.H. Vogt, eds.), Technical Report TR-90-060, International Computer Science Institute, Berkeley, August 1990. Invited lecture.
23. Thomas A. Henzinger, “A temporal logic for real time,” *Proceedings of the 22nd Annual Meeting of the Stanford Computer Forum* (N. Nilsson and J. Hennessy, eds.), Technical Report, Stanford University, February 1990.
24. Thomas A. Henzinger, *Denotational Equivalence of Goal-driven and Data-driven Interpretation of Applicative Programs*, M.S. Thesis (Thomas J. Myers, advisor), Technical Report CIS-87-05, University of Delaware, August 1986. Extended version available as Technical Report RISC-86-16.0, Kepler University, December 1986.
25. Thomas A. Henzinger, *Resolution: A Special Case of the Manna-Waldinger Relation Replacement Rule*, Technical Report CAMP-85-21.0, Kepler University, July 1985.

Lectures

Invited Lectures at Conferences

1. *From Boolean to Quantitative System Specifications*, keynote lecture, Workshop on Quantitative Analysis of Software (QA), Grenoble, France, June 2009.
2. *Quantitative Synthesis*, invited lecture, Workshop on Practical Synthesis for Concurrent Systems (PSY), Grenoble, France, June 2009.
3. *IST Austria: Building a World-Class Institute for Basic Research*, keynote lecture, Campus Opening of IST Austria, Klosterneuburg, Austria, June 2009.
4. *Designing Predictable and Robust Systems*, invited lecture, Third International Workshop on Foundations of Component-based Design (WFCD), Atlanta, Georgia, October 2008.
5. *Games in System Design and Verification*, keynote lecture, Eighth International Conference on Logic and the Foundations of Game and Decision Theory (LOFT), Amsterdam, The Netherlands, July 2008.
6. *Grand Challenges for Real-Time Systems*, keynote lecture, 20th Euromicro Conference on Real-Time Systems (ECRTS), Prague, Czech Republic, July 2008.
7. *Challenges in Embedded Systems Design: Predictability and Robustness*, invited lecture, Royal Society Meeting: From Computers to Ubiquitous Computing, London, United Kingdom, March 2008.
8. *Three Sources of Infinity in Computation: Nontermination, Real Time, and Probabilistic Choice*, keynote lecture, First International Conference on Infinity in Logic and Computation (ILC), Cape Town, South Africa, November 2007.
9. *Quantitative Generalizations of Languages*, keynote lecture, 11th International Conference on Developments in Language Theory (DLT), Turku, Finland, July 2007.
10. *Modeling, Verification, and Synthesis of Component Interfaces*, invited tutorial, 19th International Conference on Computer-Aided Verification (CAV), Berlin, Germany, July 2007.
11. *The Embedded Systems Design Challenge*, keynote lecture, 12th International Workshop on Formal Methods for Industrial-Critical Systems (FMICS), Berlin, Germany, July 2007.
12. *Path Invariants*, invited lecture, International Symposium on Automatic Heap Analysis (AHA), Berlin, Germany, July 2007.
13. *Fine-Tuning the Dial between Model Checking and Program Analysis*, invited lecture, Third Alpine Verification Meeting, Aussois, France, April 2007.
14. *Games, Time, and Probability: Graph Models for System Design and Analysis*, invited lecture, 33rd International Conference on Current Trends in Theory and Practice of Computer Science (SOFSEM), Harrachov, Czech Republic, January 2007.
15. *Timed Alternating-Time Temporal Logic*, keynote lecture, Fourth International Workshop on Formal Modeling and Analysis of Timed Systems (FORMATS), Paris, France, September 2006.
16. *Model Checking, Theorem Proving, and Abstract Interpretation: The Convergence of Formal Verification Technologies*, invited lecture, Grand Challenges of Informatics Symposium, Budapest, Hungary, September 2006.
17. *The Embedded Systems Design Challenge*, keynote lecture, 14th International Symposium on Formal Methods (FM), Hamilton, Ontario, August 2006.
18. *From Graph Models to Game Models*, invited lecture, 25 Years of Model Checking Celebration, Seattle, Washington, August 2006.

19. *Semiperfect-Information Games*, keynote lecture, 25th Annual Conference on Foundations of Software Technology and Theoretical Computer Science (FSTTCS), Hyderabad, India, December 2005.
20. *Processes as Games*, keynote lecture, 12th International Workshop on Expressiveness in Concurrency (EXPRESS), San Francisco, California, August 2005.
21. *Games in System Design and Verification*, keynote lecture, Tenth International Conference on Theoretical Aspects of Rationality and Knowledge (TARK), Singapore, June 2005.
22. *Checking Memory Safety with BLAST*, keynote lecture, Eighth International Conference on Fundamental Approaches to Software Engineering (FASE), Edinburgh, United Kingdom, April 2005.
23. *Games with Secure Equilibria: A Theory for Component Behavior*, keynote lecture, Third International Symposium on Formal Methods for Components and Objects (FMCO), Leiden, The Netherlands, November 2004.
24. *Interface-based Design*, keynote lecture, Workshop on Software Engineering Tools (The Monterey Workshop Series), Vienna, Austria, October 2004.
25. *Trading Memory for Randomness*, keynote lecture, First Annual Conference on Quantitative Evaluation of Systems (QEST), Twente, The Netherlands, September 2004.
26. *Applications of Games in System Design and Verification*, invited tutorial, Annual GAMES Workshop, Bordeaux, France, September 2004.
27. *The BLAST Query Language for Software Verification*, keynote lecture, 11th International Static Analysis Symposium (SAS), Verona, Italy, August 2004.
28. *Embedded Software: Better Models, Better Code*, keynote lecture, 25th International Conference on Applications and Theory of Petri Nets (ATPN), Bologna, Italy, June 2004.
29. *Rich Interfaces for Software Modules*, keynote lecture, 18th European Conference on Object-Oriented Programming (ECOOP), Oslo, Norway, June 2004.
30. *Game Models for Component Interaction*, keynote lecture, Workshop on Complexity, Finite Model Theory, and Databases, Lausanne, Switzerland, May 2004.
31. *Model Checking: From Hardware to Software*, keynote lecture, First Asian Symposium on Programming Languages and Systems (APLAS), Beijing, China, November 2003.
32. *The Fixed Logical Execution Time Assumption*, Workshop on Software Engineering for Embedded Systems: From Requirements to Implementation, Chicago, Illinois, September 2003.
33. *Automata for Specifying Component Interfaces*, keynote lecture, Eighth International Conference on Implementation and Application of Automata (CIAA), Santa Barbara, California, July 2003.
34. *Extreme Model Checking*, Symposium in Honor of the 64th Birthday of Zohar Manna, Taormina, Italy, June 2003.
35. *Hybrid Systems: Verification and Control*, invited tutorial, 14th International Conference on Computer-Aided Verification (CAV), Copenhagen, Denmark, July 2002.
36. *From Models to Code: The Missing Link in Embedded Software*, keynote lecture, Fifth International Workshop on Hybrid Systems: Computation and Control (HSCC), Stanford, California, March 2002.
37. *GIOTTO: A Time-Triggered Language for Embedded Programming*, First International Workshop on Embedded Software (EMSOFT), Tahoe City, California, October 2001.
38. *Decomposing Model-checking Tasks using the Assume-guarantee Paradigm*, invited tutorial, IEEE/ACM International Conference on Computer-Aided Design (ICCAD), San Jose, California, November 2000.

39. *The FRESKO Project: Formal Real-Time Software Components*, keynote lecture, First Workshop on Models for Time-critical Systems (MTCS), State College, Pennsylvania, August 2000.
40. *MASACCIO: A Formal Model for Embedded Components*, First IFIP International Conference on Theoretical Computer Science (TCS), Sendai, Japan, August 2000.
41. *A Classification of Symbolic Transition Systems*, keynote lecture, 17th International Symposium on Theoretical Aspects of Computer Science (STACS), Lille, France, February 2000.
42. *A Symbolic Approach to Hybrid Dynamical Systems*, keynote lecture, IEEE Symposium on Computational Intelligence in Robotics and Automation (CIRA), Monterey, California, November 1999.
43. *Logics for Reasoning about Real-Time Computation*, 11th International Congress of Logic, Methodology, and Philosophy of Science, Cracow, Poland, August 1999.
44. *Exploiting Design Structure in Model Checking*, 15th International Conference on the Mathematical Foundations of Programming Semantics (MFPS), New Orleans, Louisiana, April 1999.
45. *Hybrid Games*, keynote lecture, AAAI Spring Symposium Series on Artificial Intelligence, Stanford, California, March 1999.
46. *It's About Time: Real-Time Logics Reviewed*, keynote lecture, Ninth International Conference on Concurrency Theory (CONCUR), Nice, France, September 1998.
47. *Computer-Aided Verification of Embedded Systems*, 15th IFIP World Computer Congress, Vienna, Austria, August 1998.
48. *Model Checking Game Properties of Multi-agent Systems*, keynote lecture, 25th International Colloquium on Automata, Languages, and Programming (ICALP), Aalborg, Denmark, July 1998.
49. *Alternating-Time Temporal Logic*, International Symposium on Compositionality, Bad Malente-Gremsmühlen, Germany, September 1997.
50. *Some Lessons from the HYTECH Experience*, keynote lecture, International Workshop on Hybrid and Real-Time Systems (HART), Grenoble, France, March 1997.
51. *A Brief History of Real Time*, keynote lecture, First International Workshop on the Verification of Infinite-State Systems (INFINITY), Pisa, Italy, August 1996.
52. *The Theory of Hybrid Automata*, invited tutorial, 11th Annual IEEE Symposium on Logic in Computer Science (LICS), New Brunswick, New Jersey, July 1996.
53. *HYTECH in Control Applications*, DIMACS Workshop on Controllers for Manufacturing and Automation: Specification, Synthesis, and Verification Issues, New Brunswick, New Jersey, May 1996.
54. *Algorithmic Analysis of Hybrid Systems*, Third SIAM Conference on Control and its Applications, St. Louis, Missouri, April 1995.
55. *Model-Checking and Abstract-Interpretation Strategies for Hybrid Systems*, Workshop on Hybrid Systems and Autonomous Control, Ithaca, New York, October 1994.
56. *Model-Checking Strategies for Linear Hybrid Systems*, Seventh International Conference on Industrial and Engineering Applications of Artificial Intelligence and Expert Systems (IEA/AIE), Austin, Texas, May 1994.
57. *Real-Time System = Discrete System + Clock Variables*, First AMAST Workshop on Real-Time Systems (ARTS), Iowa City, Iowa, November 1993.
58. *Real-Time Formalisms for Verification*, invited tutorial, Fifth International Conference on Computer-Aided Verification (CAV), Elounda, Greece, June 1993.

59. *Sooner Is Safer Than Later*, Eighth Summer Conference on General Topology and Applications, New York, New York, June 1992.
60. *Timed Transition Systems*, REX Workshop on Real-Time Systems (REX), Mook, The Netherlands, June 1991.

Distinguished Speaker Series

1. *Interface-based Design*, Computer Science Colloquium, Technical University Vienna, Austria, March 2009.
2. *Grand Challenges for Real-Time Systems*, Computer Science Colloquium, University of York, United Kingdom, November 2008.
3. *Rich Interfaces for Reactive, Real-Time, and Embedded Components*, Computer Science Colloquium, Technical University Eindhoven, The Netherlands, March 2007.
4. *Reliable Systems Engineering*, Inaugural Lecture, EPFL, Switzerland, December 2006.
5. *Reliable Systems Engineering*, Strachey Lecture in Computing Science, Oxford University, United Kingdom, October 2006.
6. *Games, Time, and Probability: Models and Algorithms for System Design and Analysis*, Wolfgang Pauli Colloquium, Technical University Vienna, Austria, May 2005.
7. *The Symbolic Approach to Hybrid Systems*, Hybrid Systems Lecture Series, Ecole Polytechnique, Orsay, France, April 2005.
8. *Embedded Software: Better Models, Better Code*, Jon Postel Distinguished Lecture, University of California, Los Angeles, February 2004.
9. *The Symbolic Approach to Hybrid Systems*, Mathematics and Computer Science Colloquium, Santa Clara University, Santa Clara, California, January 2004.
10. *From Models to Code: The Missing Link in Embedded Software*, Computer Science Colloquium, Purdue University, West Lafayette, Indiana, April 2003.
11. *From Models to Code: The Missing Link in Embedded Software*, Computer Science Colloquium, University of Delaware, Newark, Delaware, April 2003.
12. *The Symbolic Approach to Hybrid Systems*, Systems Engineering Colloquium, University of Maryland, College Park, Maryland, April 2003.
13. *Games, Time, and Probability: Models and Algorithms for System Design and Analysis*, Computer Science Colloquium, EPF Lausanne, Switzerland, February 2003.
14. *What is an Interface?*, Computer Science Colloquium, ETH Zürich, Switzerland, June 2002.
15. *From Models to Code: The Missing Link in Embedded Software*, Computer Science Colloquium, Technical University Munich, Germany, May 2002.
16. *Design and Verification of Embedded Systems*, Cray Distinguished Lecture, University of Minnesota, Minneapolis, Minnesota, September 2001.
17. *A Symbolic Approach to Hybrid Dynamical Systems*, Computer Science Colloquium, University of Brussels, Belgium, February 2000.
18. *A Symbolic Approach to Hybrid Dynamical Systems*, Computer Science Colloquium, University of Pennsylvania, Philadelphia, Pennsylvania, October 1999.

19. *Computer-Aided Verification of Embedded Systems*, Computer Science Colloquium, Max-Planck-Institut für Informatik, Saarbrücken, Germany, March 1997.
20. *Computer-Aided Verification of Embedded Systems*, Computer Science Colloquium, University of British Columbia, Vancouver, Canada, January 1997.
21. *A Brief History of Real Time*, Electrical Engineering and Computer Sciences Colloquium, University of California, Berkeley, California, April 1996.
22. *Hybrid Automata*, Computer Science Colloquium, University of Delaware, Newark, Delaware, April 1993.
23. *Hybrid Automata*, Computer Science Colloquium, Cornell University, Ithaca, New York, October 1992.

Invited Lectures at Summer Schools

1. *Games in System Design and Verification*, 17th International School for Computer Science Researchers on Formal Methods: Theory and Practice, Lipari, Italy, July 2005.
2. *Interface-based Design*, lecture series, NATO International Summer School on Engineering Theories for Software-intensive Systems, Marktoberdorf, Germany, August 2004.
3. *Interface Theories for Component-based Design and Verification*, University of Washington and Microsoft Research Summer Institute on Specifying and Checking Properties of Software, Leavenworth, Washington, August 2001.
4. *Rectangular Hybrid Automata*, lecture series, NATO-ASI International Summer School on the Verification of Digital and Hybrid Systems, Antalya, Turkey, June 1997.
5. *Models and Logics for Timed and Hybrid Systems: An Introduction*, School on Methods and Tools for the Verification of Infinite-State Systems, Grenoble, France, March 1997.
6. *Automatic Verification of Real-Time and Hybrid Systems*, lecture series, BRICS International Autumn School on Verification, Aarhus, Denmark, October 1996.

Other Invited Lectures

1. *Theories for Encompassing Heterogeneity*, COMBEST Annual Review, Brussels, Belgium, January 2009.
2. *Quantitative Generalizations of Languages*, Annual AutoMathA Workshop, Lausanne, Switzerland, May 2008.
3. *Designing Predictable and Robust Systems*, Workshop: From Embedded Systems to Cyber-Physical Systems, St. Louis, Missouri, April 2008.
4. *Trends and Challenges in Embedded Systems and Computing*, European Commission, Brussels, Belgium, December 2007.
5. *Nonzero-sum Games in Verification and Synthesis*, Annual GAMES Workshop, Lausanne, Switzerland, September 2007.
6. *Value Iteration Fixpoints for Verification and Control*, CHES Review, Berkeley, California, February 2007.
7. *On Component and Interface Models*, ARTIST Workshop on Models of Computation and Communication, Zürich, Switzerland, November 2006.
8. *Software Verification*, Unicile, Lausanne, Switzerland, April 2006.

9. *The BLAST Model Checker*, Workshop on Software Verification, Hyderabad, India, December 2005.
10. *Model Checking: From Graphs to Games*, First Alpine Verification Meeting, Lausanne, Switzerland, October 2005.
11. *Games in System Design and Verification*, Microsoft Research, Redmond, Washington, August 2005.
12. *Interfaces for Compositional Real-Time Code*, ARTIST PI Meeting, Rennes, France, June 2005.
13. *Model Checking: From Hardware to Software*, Annual IC Research Day, EPFL, Lausanne, Switzerland, June 2005.
14. *Discounting the Future in Systems Theory*, CHESS Seminar, Berkeley, California, March 2005.
15. *TRESOR: Trust in Reliable Software Research*, IBM Research Center, Zürich, Switzerland, September 2004.
16. *The Holy Grail of Computer Science: Automatic Program Verification*, Federal Verification Center, University of Brussels, Belgium, May 2004; Research Institute for Symbolic Computation, University of Linz, Austria, August 2004; Ecole Normale Supérieure, Cachan, France, April 2005.
17. *Hybrid Systems Theory*, CHESS Review, Berkeley, California, December 2003; CHESS Review, Berkeley, California, May 2004; CHESS Review, Berkeley, California, November 2004; CHESS Review, Berkeley, California, May 2005; CHESS Review, Berkeley, California, October 2006.
18. *Embedded Software: Better Models, Better Code*, CHESS Workshop, Berkeley, California, September 2003; ARTIST Collaboration Day, Philadelphia, Pennsylvania, October 2003; Industrial Liaison Program, University of California, Berkeley, California, February 2004; Robotics Seminar, EPFL, Lausanne, Switzerland, November 2004; ARTIST PI Meeting, Paris, France, January 2005.
19. *Interfaces for Resource-constrained Components*, GSRC Annual Review, Santa Clara, California, September 2003.
20. *Thread-modular Abstraction Refinement*, Fujitsu Labs, Mountain View, California, June 2003.
21. *An Update on the FRESKO Project*, GSRC PI Meeting, Oakland, California, March 2003.
22. *Software Quality Research at Berkeley*, Microsoft CITRIS Day, Berkeley, California, March 2003.
23. *Games, Time, and Probability: Models and Algorithms for System Design and Analysis*, ETH Zürich, Switzerland, February 2003.
24. *Automatic Software Verification: Code Checking, not Model Checking*, NASA PI Meeting, Mountain View, California, February 2002.
25. *Interface-based Design*, GSRC PI Meeting, Berkeley, California, December 2002.
26. *Hybrid Systems: From Models to Code*, CHESS Kickoff Meeting, Berkeley, California, November 2002; DARPA Software-Enabled Control PI Meeting, Atlanta, Georgia, November 2002.
27. *From Models to Code: The Missing Link in Embedded Software*, GM CHESS Day, Berkeley, California, October 2003.
28. *Model-based Design of Embedded Systems*, CHESS Workshop, Berkeley, California, September 2002.
29. *AUTOTIMESAFE: Predictable, Portable Real-Time Software*, DaimlerChrysler, Esslingen, Germany, May 2002.
30. *Interface Compatibility Checking*, SRC Formal Verification Review, Salt Lake City, Utah, March 2002.

31. *CHES: Center for Hybrid and Embedded Software Systems*, Industrial Advisory Board Meeting for UC Berkeley EECS, Palo Alto, California, October 2001; BMW CHES Day, Berkeley, California, January 2002; DaimlerChrysler, Stuttgart, Germany, July 2002; National Technology Agency of Finland CHES Day, Berkeley, California, September 2002; EECS Faculty Lunch, Berkeley, California, October 2002; CHES Industrial Day, Berkeley, California, May 2003.
32. *Embedded Control Systems Development with GIOTTO*, Audi, Ingolstadt, Germany, September 2001; Honeywell Technology Center, Minneapolis, Minnesota, September 2001.
33. *What is an Interface?*, GSRC Annual Review, Santa Clara, California, September 2001; DARPA Program Composition for Embedded Systems PI Meeting, Mesa, Arizona, October 2001; Software Engineering Seminar, Technical University Munich, Germany, May 2002.
34. *Interface Theories for Component-based Design*, NSF Open Source Quality Retreat, Santa Cruz, California, May 2001; Verification Seminar, Stanford University, Stanford, California, August 2001.
35. *The Embedded Virtual Machine*, DARPA Software-Enabled Control PI Meeting, Annapolis, Maryland, May 2001.
36. *Model Checking of Infinite-State Systems*, Microsoft Research, Redmond, Washington, May 2001.
37. *MASACCIO: A Formal Model for Embedded Components*, GSRC PI Meeting, Pittsburgh, Pennsylvania, March 2001.
38. *The Control of Synchronous Systems*, SRC Formal Verification Review, Pittsburgh, Pennsylvania, March 2001.
39. *Decomposing Model-checking Tasks using the Assume-guarantee Paradigm*, IFIP Working Group 2.3 Meeting, Santa Cruz, California, January 2001.
40. *GIOTTO: A Time-Triggered Language for Embedded Programming*, DARPA Software-Enabled Control PI Meeting, Durango, Colorado, October 2000; Compaq Systems Research Center, Palo Alto, California, April 2001; Wind River Systems, Alameda, California, April 2001.
41. *Formal Software Verification*, GSRC PI Meeting, Stanford, California, September 2000.
42. *The FRESKO Project: Formal Real-Time Software Components*, GSRC PI Meeting, Los Angeles, California, June 2000; DARPA Software-Enabled Control PI Meeting, Albuquerque, New Mexico, June 2000.
43. *Fault-Tolerant Real-Time Networks*, MURI Kickoff Meeting, Berkeley, California, May 2000.
44. *From Hybrid Models to Time-Triggered Implementations*, DARPA Workshop on Software-Enabled Control, St. Louis, Missouri, April 2000.
45. *Early Detection of Counterexamples in Model Checking*, SRC Formal Verification Review, Austin, Texas, March 2000.
46. *Games in Formal Verification*, DARPA Formal Methods PI Meeting, Portland, Oregon, March 2000.
47. *Formal Models and Interoperability*, GSRC Annual Review, San Jose, California, December 1999; DARPA Workshop on High Confidence Aviation Systems, Alexandria, Virginia, June 2000.
48. *Assume-Guarantee Refinement Between Different Time Scales*, Intel Corporation, Santa Clara, California, November 1999.
49. *Interval-numerical Methods for Hybrid-systems Analysis*, MURI Annual Review, Berkeley, California, November 1999.
50. *Exploiting Design Structure in Model Checking*, DARPA Formal Methods PI Meeting, Alexandria, Virginia, October 1999; Synopsis Corporation, Mountain View, California, June 2000.

51. *A Research Agenda for Component Software Technology*, DARPA Workshop on Embedded Components, Berkeley, California, September 1999.
52. *Reactive and Hybrid Systems*, Biennial Scientific Review, Max-Planck Institute for Computer Science, Saarbrücken, Germany, June 1999.
53. *Formal Verification vs. Formalizable Design*, DARPA Workshop on Software Behavior Description, St. Thomas, U.S. Virgin Islands, December 1998.
54. *A Tutorial Introduction to Hybrid Systems Modeling and Verification*, Hybrid Systems Seminar, University of California, Berkeley, California, September 1998.
55. *Model Checking Game Properties of Multi-agent Systems*, Verification Seminar, Stanford University, Stanford, California, June 1998; MURI Annual Review, Berkeley, California, November 1998.
56. *MOCHA: Modularity in Model Checking*, DARPA Formal Methods PI Meeting, Seattle, Washington, June 1998.
57. *Logics for Reasoning about Time*, Logic Colloquium, University of California, Berkeley, California, April 1998.
58. *You Assume, We Guarantee: Methodology and Case Studies*, SRC Formal Verification Review, Austin, Texas, March 1998; DARPA Formal Methods PI Meeting, Stanford, California, October 1998.
59. *Software-based Control of Multi-agent Systems*, DARPA Workshop on Software-Enabled Control, Atlanta, Georgia, December 1997.
60. *Concurrency Modeling: The State View*, Design Seminar, University of California, Berkeley, California, November 1997.
61. *Some Lessons from the HYTECH Experience*, AFOSR Software and Systems PI Meeting, Rome, New York, September 1997.
62. *Two Approaches to the Nonzeno Problem in Hybrid Control*, Cadence European Labs, Rome, Italy, July 1997; NASA Ames Research Center, Mountain View, California, August 1997; MURI Annual Review, Berkeley, California, October 1997.
63. *Formal Verification of Embedded Systems*, Industrial Liaison Program, University of California, Berkeley, California, March 1997; Scientific Systems, Woburn, Massachusetts, May 2001.
64. *Efficient Verification of Heterogeneous Systems*, SRC Formal Verification Review, Pittsburgh, Pennsylvania, March 1997.
65. *Efficient Formal Verification using Transition Hierarchies*, Intel Corporation, Hillsboro, Oregon, January 1997.
66. *A Brief History of Real Time*, University of Oldenburg, Oldenburg, Germany, October 1996; SRI International, Menlo Park, California, November 1996.
67. *Design and Verification of Hybrid Systems*, MURI Kickoff Meeting, Berkeley, California, July 1996; DARPA Workshop on Complex Systems, Portland, Oregon, August 1997.
68. *Reactive Modules*, Verification Seminar, Stanford University, Stanford, California, May 1996.
69. *New Looks at Old Concepts: Local Liveness and Finitary Fairness*, CAD Seminar, University of California, Berkeley, California, March 1996.
70. *A Unifying Framework for Computer-Aided Verification*, SRC Formal Verification Review, Berkeley, California, March 1996.

71. *Algorithmic Analysis of Real-Time and Hybrid Systems*, DARPA Formal Methods PI Meeting, San Diego, California, January 1996; Industrial Liaison Program, University of California, Berkeley, California, March 1996; ONR Workshop on Automated Formal Methods, Oxford, United Kingdom, June 1996; University of Passau, Passau, Germany, September 1996.
72. *A Reactive-Module Approach to Formal Design, Verification, and Synthesis*, SRC Annual Review, Berkeley, California, October 1995.
73. *Computer-Aided Verification of Infinite-State Systems*, University of California, Berkeley, California, March 1995; Stanford University, Stanford, California, March 1995; AT&T-SUNY Specification and Verification Workshop, Stony Brook, New York, November 1995; Synopsis Corporation, Mountain View, California, June 1996.
74. *Model-Checking and Abstract-Interpretation Strategies for Hybrid Systems*, AFOSR Software and Systems PI Meeting, Washington, DC, September 1994; Cornell University, Ithaca, New York, October 1994; Stanford University, Stanford, California, January 1995.
75. *Algorithmic Analysis of Hybrid Systems*, Stanford University, Stanford, California, August 1994.
76. *A Determinizable Class of Timed Automata*, University of California, Berkeley, California, March 1994.
77. *Real-Time System = Discrete System + Clock Variables*, Fourth North American Jumelage, SRI International, Menlo Park, California, October 1993.
78. *Computing Accumulated Delays in Real-Time Systems*, Stanford University, Stanford, California, August 1993.
79. *Hybrid Automata*, Mathematical Sciences Institute, Ithaca, New York, February 1993; Stanford University, Stanford, California, March 1993; University of California, Berkeley, California, March 1993; AT&T Bell Laboratories, Murray Hill, New Jersey, May 1993.
80. *Some Recent Progress and Some Open Problems in Timed Model Checking*, ONR Workshop on Formal Methods in Software Engineering, Monterey, California, May 1992.
81. *Symbolic Model Checking for Real-Time Systems*, Cornell University, Ithaca, New York, April 1992; Carnegie-Mellon University, Pittsburgh, Pennsylvania, April 1992; AT&T Bell Laboratories, Murray Hill, New Jersey, May 1992; Stanford University, Stanford, California, August 1993.
82. *Logics and Models of Real Time*, ORA Corporation, Ithaca, New York, February 1992.
83. *Back to the Future*, Fourier University, Grenoble, France, December 1991; Cornell University, Ithaca, New York, April 1992; Research Institute for Symbolic Computation, Hagenberg, Austria, July 1992; Stanford University, Stanford, California, March 1993.
84. *Sooner Is Safer Than Later*, Fourier University, Grenoble, France, November 1991; Cornell University, Ithaca, New York, February 1992; Technical University Eindhoven, The Netherlands, November 1991.
85. *The Temporal Specification and Verification of Real-Time Systems*, Ph.D. Thesis Defense, Stanford University, Stanford, California, June 1991; Berkeley Workshop on Temporal and Real-Time Specification, International Computer Science Institute, Berkeley, California, August 1990.
86. *When Eventually Isn't Good Enough*, Stanford Computer Forum, Stanford University, Stanford, California, February 1991; University of California, Santa Barbara, California, February 1991; Rice University, Houston, Texas, February 1991; University of Toronto, Toronto, Canada, February 1991; AT&T Bell Laboratories, Murray Hill, New Jersey, February 1991; Cornell University, Ithaca, New York, February 1991; SRI International, Menlo Park, California, March 1991; University of British Columbia, Vancouver, Canada, March 1991; University of California, San Diego, California, March 1991; Columbia University, New York, New York, March 1991; State University of New York, Stony Brook, New York, March 1991; Bell Communications Research, Morristown, New Jersey, March 1991; Xerox Palo Alto Research Center, Palo Alto, California, April 1991.

87. *Temporal Logic and Real Time*, SRI International, Menlo Park, California, August 1990; Kepler University, Linz, Austria, October 1990; The Technion, Haifa, Israel, October 1990.
88. *A Temporal Logic for Real Time*, Stanford Computer Forum, Stanford University, Stanford, California, February 1990.
89. *A Really Temporal Logic*, Stanford University, Stanford, California, October 1989; IBM T.J. Watson Research Center, Yorktown Heights, New York, December 1989.

Panels

1. Moderator, *Institutes of Basic Research: Is There One Formula for Success?*, Campus Opening of IST Austria, Klosterneuburg, Austria, June 2009.
2. Moderator, *Embedded Software: A New Research Community?*, First International Workshop on Embedded Software (EMSOFT), Tahoe City, California, October 2001.
3. Panelist, *Hybrid Systems*, NSF-DARPA Workshop on Future Directions in Hybrid and Embedded Systems, Alexandria, Virginia, October 2000.
4. Panelist, *Formal Methods*, DARPA Workshop on High Confidence Aviation Systems, Alexandria, Virginia, June 2000.
5. Panelist, *Future Directions of EDA Research at Berkeley*, Electrical Engineering and Computer Sciences Colloquium, University of California, Berkeley, California, October 1997.
6. Panelist, *Future Trends in Industrial Computer-Aided Verification*, Ninth International Conference on Computer-Aided Verification (CAV), Haifa, Israel, June 1997.
7. Moderator, *Hybrid Systems Research: Achievements, Problems, and Goals*, DIMACS Workshop on Verification and Control of Hybrid Systems, New Brunswick, New Jersey, October 1995.

University Activities

Administration

Director, *Center for Hybrid and Embedded Software Systems* (CHESS), University of California, Berkeley, since 2002.

Director, *Doctoral Program in Computer and Communication Sciences* (EDIC), EPFL, since 2007.

Instruction

Lower-division undergraduate courses:

Theoretical Computer Science, EPFL, Spring 2008, Spring 2009.

Advanced Theoretical Computer Science, EPFL, Spring 2008.

Theoretical Computer Science III, EPFL, Fall 2005, Fall 2006.

Structure and Interpretation of Signals and Systems, UC Berkeley (EECS 20), Spring 2001.

Discrete Mathematics, Cornell (CS 280), Fall 1992, Fall 1993, Fall 1994.

Upper-division undergraduate courses:

Efficient Algorithms and Intractable Problems, UC Berkeley (CS 170), Fall 2002.

Computability and Complexity, UC Berkeley (CS 172), Spring 1997, Fall 1997, Spring 2000, Fall 2000, Fall 2003.

Design and Analysis of Algorithms, Cornell (CS 482), Spring 1992.

Automated Reasoning, Stanford (CS 157), Winter 1989.

Graduate courses:

Problem Solving in Computer Science, EPFL, Spring 2005, Spring 2007, Fall 2008.

Model Checking: From Finite-State to Hybrid Systems, Technical University Munich, May 2002; ETH Zürich, June 2002; EPFL, Fall 2007.

Formal Techniques for Software Reliability, UC Berkeley (CS 294-8), Spring 2001 (taught jointly with Alex Aiken and George Necula).

Hybrid Systems, UC Berkeley (EECS 291E), Fall 1996 (taught jointly with Shankar Sastry), Spring 2000 (taught jointly with Karl Johansson).

Computer-Aided Verification, UC Berkeley (EECS 219C), Spring 1996, Spring 1998, Fall 1999, Spring 2003; University of the Saarland, Spring 1999; EPFL, Fall 2004, Spring 2006.

Advanced Programming Languages, Cornell (CS 611), Fall 1995.

Theory of Concurrent Systems, Cornell (CS 615), Spring 1993, Spring 1994, Spring 1995.

Graduate seminars:

Topics in Formal Methods, Cornell (CS 713), Fall 1994.

Technical Writing, Cornell (CS 713), Fall 1993.

Topics in Hybrid Systems, Cornell (CS 713), Fall 1992, Fall 1995.

Advising

Undergraduate advisor in Computer Science at EPFL, since 2005; in Computer Engineering at UC Berkeley, 2002–04; in Computer Science at Cornell University, 1992–95.

Semester project supervisor at EPFL for Regis Blanc (*ABC: Analyzing Bounds and Complexity*, 2009); Thibaud Hottelier (*VALIDATOR: A Verification Tool with Bound and Invariant Generation*, 2008); Polina Makeeva (*Regular Membership Constraints*, 2008).

Bachelor's thesis supervisor at EPFL for Christoph Trunk (*On Games of Imperfect Information*, 2007).

Master's advisor in Computer Science at EPFL, since 2005; M.Eng. advisor in Computer Science at Cornell University, 1992–95.

Master's thesis supervisor at EPFL for Damien Zufferey (*Verification of Concurrent Asynchronous Message-Passing Programs*, 2009); Thibaud Hottelier (*Invariants for Arrays and Matrices*, 2009); Cédric Favre (*Algorithmic Verification of Business Process Models*, 2008); Yvan Bidiville (*An Extended Instrumentation Language for Fault Localization*, 2007); Ashutosh K. Gupta (*Proving Termination of Program Loops*, 2007); Marc Schaub (*Formal Methods in Systems Biology: Modeling the Notch/Wnt Crosstalk in Mammalian Skin*, 2006), won the EPFL Foundation Anaheim Award 2006; Grégory Théoduloz (*Integrating Shape Analysis in the Model Checker BLAST*, 2006), won the EPFL Unicile Award 2006.

M.S. thesis supervisor at UC Berkeley for Arindam Chakrabarti (*Interface Compatibility Checking for Software Modules*, 2005), Krishnendu Chatterjee (*Stack-Size Analysis for Interrupt-driven Programs*, 2004), Arkadeb Ghosal (*XGIOTTO: Event-driven Programming*, 2004), Shaz Qadeer (*Partial-order Reduction in Symbolic State-space Exploration*, 1997).

M.S. thesis reader at UC Berkeley for Jongho Lee (*New Real-Time Embedded Software for an Autonomous Helicopter System using Giotto*, 2006), N. Vinay Krishnan (*Real-Time Systems Design in Ptolemy II: A Time-triggered Approach*, 2004), Judy Liebman (*The Time-based Approach to Embedded Programming: A Hardware-in-the-Loop Simulation Framework*, 2002), Paul Whitaker (*The Simulation of Synchronous Reactive Systems in Ptolemy II*, 2001), Minxi Gao (*Multi-valued Multi-level Logic Synthesis*,

- 2000), Jun Zhang (*Dynamical Systems Revisited: Hybrid Systems with Zeno Executions*, 1999), Mudit Goel (*Process Networks in Ptolemy II*, 1998), Neil Smyth (*CSP Domain in Ptolemy II*, 1998), Gurmeet S. Manku (*Structural Symmetries and Model Checking*, 1997), Cliff Cordeiro (*The Tycho Interactive Documentation System*, 1997), Farhana Sheikh (*Visualizing Architecture and Algorithm Interaction in Embedded Systems*, 1997), Alok Agrawal (*Compact Complete Test Sets for Multiple Stuck Faults*, 1996), Adrian Isles (*Formal Verification using the Integer Combinational/Sequential Concurrency Model*, 1996), Bilung Lee (*Fusing Data Flow with Finite State Machines*, 1996).
- Ph.D. preliminary examiner in Computer-Aided Design at UC Berkeley, Fall 1996, Spring 1997, Fall 1997, Spring 1998, Fall 1999, Spring 2000, Fall 2000, Spring 2001, Spring 2003.
- Ph.D. qualifying examiner at UC Berkeley for Arkadeb Ghosal (electrical engineering, 2006), Krishnendu Chatterjee (computer science, 2004), Slobodan Matic (electrical engineering, 2004), Arindam Chakrabarti (computer science, 2004), Vinayak S. Prabhu (electrical engineering, 2004), Ranjit Jhala (computer science, 2003), Westley Weimer (computer science, 2003), Stephen Neuendorffer (electrical engineering, 2003), Joseph Flenner (mathematics, 2003), Xiaojun Liu (electrical engineering, 2003), Rupak Majumdar (computer science, 2002), Deepak Rajan (industrial engineering and operations research, 2001), Benjamin Horowitz (computer science, 2001), David Andre (computer science, 2000), Daishi Harada (computer science, 2000), Clifton Ealy (mathematics, 2000), Alf Onshuus (mathematics, 2000), Yuhong Xiong (electrical engineering, 2000), Jie Liu (electrical engineering, 1999), Freddy Y.C. Mang (computer science, 1999), John Davis (electrical engineering, 1998), Adrian Isles (electrical engineering, 1998), Bilung Lee (electrical engineering, 1998), Shaz Qadeer (electrical engineering, 1998), Sriram K. Rajamani (computer science, 1998), Amit Narayan (electrical engineering, 1997), George Pappas (electrical engineering, 1997), Serdar Tasiran (electrical engineering, 1996), Claire Tomlin (electrical engineering, 1996), Hal Wasserman (computer science, 1996).
- Ph.D. jury president at EPFL for Radu Jurca (*Truthful Reputation Mechanisms for Online Systems*, 2007).
- Ph.D. thesis reader at UC Berkeley for Xiaojun Liu (*Semantic Foundation of the Tagged Signal Model*, 2005), Deepak Rajan (*Designing Capacitated Survivable Networks: Polyhedral Analysis and Algorithms*, 2004), Yuhong Xiong (*An Extensible Type System for Component-based Design*, 2002), John Davis (*Order, Concurrency, and System-level Design*, 2000), George Pappas (*Hybrid Systems: Computation and Abstraction*, 1998), Claire Tomlin (*Hybrid Control of Air-traffic Management Systems*, 1998); at Cornell University for Eric Aaron, Jason Hickey, Rod Moten, David Sturgill, Samuel Weber (1992–95).
- Ph.D. thesis referee for Dejan Ničković (*Checking Timed and Hybrid Properties: Theory and Applications*, University of Grenoble, France, 2008); Corneliu Popeea (*Disjunctive Invariants for Modular Static Analysis*, National University of Singapore, 2008); Jérémie Cabessa (*A Game-theoretical Approach to the Algebraic Counterpart of the Wagner Hierarchy*, University of Paris 7, France, 2007); Laurent Doyen (*Algorithmic Analysis of Complex Semantics for Timed and Hybrid Automata*, University of Brussels, Belgium, 2006); Cristina Cerschi Seceleanu (*A Methodology for Constructing Correct Reactive Systems*, University of Turku, Finland, 2005); Maria Sorea, (*Verification of Real-Time Systems through Lazy Approximations*, University of Ulm, Germany, 2004); Mauno Rönkkö (*Stepwise Development of Hybrid Systems*, University of Turku, Finland, 2001); Jean-François Raskin (*Logics, Automata, and Classical Theories for Deciding Real Time*, University of Namur, Belgium, 1999); Gunnar Wittich (*Ein problemorientierter Ansatz zum Nachweis von Realzeiteigenschaften eingebetteter Systeme*, University of Oldenburg, Germany, 1999); Yonit Kesten (*Refinement and Verification of Reactive and Hybrid Systems*, The Weizmann Institute of Science, Israel, 1995).
- Postdoctoral supervisor at Cornell University for Howard Wong-Toi (1994–96); at UC Berkeley for Orna Kupferman (1996–98), Luca de Alfaro (1997–2001), Sriram Krishnan (1998–99), Jean-François Raskin (1998–2000), Christoph M. Kirsch (1999–2004), Marius Minea (2000–01), Grégoire Sutre (2000–01), Marcin Jurdziński (2000–04), Marco A. Sanvido (2002–04), Dirk Beyer (2003–04); at EPFL for Dirk Beyer (2004–06), Jasmin Fisher (2004–07), Nir Piterman (2004–07), Andrey Rybalchenko (2006–07; won a Microsoft postdoctoral fellowship), Laurent Doyen (since 2006), Dietmar Berwanger (2007),

Barbara Jobstmann (since 2007), Laura Kovács (since 2007), Verena Wolf (since 2008), Thomas Wies (since 2008), Dejan Nickovic (since 2008).

Departmental and University Committees

Stanford University: graduate student representative, 1989–90.

Cornell University: Computer Science Faculty Recruiting Committee, 1993–94, 1994–95; Computer Science Distinguished Lecture Series Coordinator, 1993–94, 1994–95; Computer Science Cognitive Studies Liaison, 1993–94, 1994–95, Fall 1995; Computer Science Undergraduate Curriculum Committee, 1992–93; Computer Science M.Eng. Curriculum Committee, 1992–93; graduate field member for Computer Science, Applied Mathematics, and Cognitive Studies, 1992–95.

University of California, Berkeley: EECS Faculty Recruiting Committee, 2002–03; EECS NASA Ames Task Force, 2000–01; EECS Alumni Outreach Task Force, 2000–01; EECS Coordination and Planning Task Force, 1996–97; EECS Undergraduate Admissions Committee, 1999–2000 (chair); EECS Undergraduate Curriculum Committee, 1997–98, 1998–99, 1999–2000; EECS Graduate Admissions Committee, 1996–97, 1997–98; EECS Math 55 Task Force, 1998–99; EECS Retreat Committee, 1996–97, 1997–98.

University of the Saarland: Mathematics for Computer Science Task Force, 1999; International Graduate School Task Force, 1999.

EPFL: EPFL Faculty Promotion Committee, since 2004; IC Executive Committee, since 2007; IC Faculty Promotion Committee, 2007–08; IC Doctoral School Committee, 2006–07; IC MsPhD Program Task Force, 2006–07; IC Faculty Recruiting Committee, 2004–06.

Professional Activities

Conference Organizer and Program Chair

1. *Workshop: From Embedded Systems to Cyber-Physical Systems*, St. Louis, Missouri, April 2008 (jointly organized and chaired with Alberto Sangiovanni-Vincentelli, Jonathan Sprinkle, and Janos Sztipanovits).
2. *Second International Workshop on Foundations of Component-based Design (WFCD)*, Salzburg, Austria, September 2007 (jointly organized and chaired with Werner Damm).
3. *Annual IC Research Day*, EPFL, Lausanne, Switzerland, June 2005.
4. *Workshop on Software Engineering Tools* (The Monterey Workshop Series), Vienna, Austria, October 2004 (jointly organized and chaired with Zohar Manna).
5. *First International Workshop on Embedded Software (EMSOFT)*, Tahoe City, California, October 2001.
6. *First International Workshop on Hybrid Systems: Computation and Control (HSCC)*, Berkeley, California, April 1998 (jointly organized and chaired with Shankar Sastry).
7. *Eighth International Conference on Computer-Aided Verification (CAV)*, New Brunswick, New Jersey, July 1996 (jointly organized and chaired with Rajeev Alur).
8. *DIMACS Workshop on Verification and Control of Hybrid Systems*, New Brunswick, New Jersey, October 1995 (jointly organized and chaired with Rajeev Alur and Eduardo D. Sontag).

Conference Program Chair

1. *16th International Conference on Computer Science Logic (CSL)*, Lausanne, Switzerland, September 2007 (jointly chaired with Jacques Duparc).

Member of Conference Program Committees

1. Second International AutoMathA Conference, 2009.
2. Fourth Annual ACM Workshop on Transactional Computing (TRANSACT), 2009.
3. 12th International Workshop on Hybrid Systems: Computation and Control (HSCC), 2009.
4. Third International IFAC Conference on Analysis and Design of Hybrid Systems (ADHS), 2009.
5. 35th International Conference on Current Trends in Theory and Practice of Computer Science (SOFSEM), 2009.
6. IFIP Working Conference on Domain Specific Languages (DSL), 2009.
7. International Conference on Logic Programming and Automated Reasoning (LPAR), 2008.
8. International Workshop on Model-based Architecturing and Construction of Embedded Systems (ACES), 2008.
9. 29th Annual IEEE Real-Time Systems Symposium (RTSS), 2008.
10. 4th International Workshop on Automated Specification and Verification of Web Systems (WWV), 2008.
11. 6th Annual IEEE Conference on Software Engineering and Formal Methods (SEFM), 2008.
12. 20th International Conference on Computer-Aided Verification (CAV), 2008.
13. First International Workshop on Cyber-Physical Systems (CPS), 2008.
14. Eighth International School on Modeling and Verifying Parallel Processes (MOVEP), 2008.
15. First International Workshop on Model-driven High-level Programming of Embedded Systems (SLAP), 2008.
16. Ninth International Conference on Verification, Model Checking, and Abstract Interpretation (VMCAI), 2008.
17. Fourth Annual IEEE Conference on Quantitative Evaluation of Systems (QEST), 2007.
18. 22nd Annual IEEE Symposium on Logic in Computer Science (LICS), 2007.
19. 34th Annual Symposium on Principles of Programming Languages (POPL), 2007.
20. Design, Automation, and Test in Europe (DATE), 2007.
21. First International Conference on Tests and Proofs (TAP), 2007.
22. First International Workshop on Testing, Analysis, and Verification of Web Services and Applications (TAV-WEB), 2006.
23. 12th Annual IEEE Conference on Embedded and Real-Time Computing Systems and Applications (RTCSA), 2006.
24. Sixth Annual ACM Conference on Embedded Software (EMSOFT), 2006.
25. Second International Conference on Intelligent Computer Communication and Processing (ICCP), 2006.
26. 33rd International Colloquium on Automata, Languages, and Programming (ICALP), 2006.
27. International Symposium on Component-Based Software Engineering (CBSE), 2006.

28. International Joint Conference on Automated Reasoning (IJCAR), 2006.
29. Joint Modular Languages Conference (JMLC), 2006.
30. Sixth International Conference on Application of Concurrency to System Design (ACSD), 2006.
31. 11th International Workshop on Formal Methods for Industrial Critical Systems (FMICS), 2006.
32. Design, Automation, and Test in Europe (DATE), 2006.
33. Third International Colloquium on Theoretical Aspects of Computing (ICTAC), 2006.
34. International Conference on Foundations of Software Science and Computation Structures (FOSSACS), 2006.
35. Second International IFAC Conference on Analysis and Design of Hybrid Systems (ADHS), 2006.
36. Fifth International Symposium on Software Composition (SC), 2006.
37. Second International Workshop on Software Quality (SOQUA), 2005.
38. 11th Annual IEEE Conference on Embedded and Real-Time Computing Systems and Applications (RTCSA), 2005.
39. First International Workshop on Foundations of Interface Technologies (FIT), 2005.
40. Fifth Annual ACM Conference on Embedded Software (EMSOFT), 2005.
41. Fifth International Conference on Application of Concurrency to System Design (ACSD), 2005.
42. First International Workshop on Verification and Validation of Model-based Planning and Scheduling Systems (VVPS), 2005.
43. 17th International Conference on Computer-Aided Verification (CAV), 2005.
44. International Symposium on Component-Based Software Engineering (CBSE), 2005.
45. First International Embedded and Hybrid Systems Conference (IEHSC), 2005.
46. 13th International Symposium on Formal Methods (FM), 2005.
47. Eighth International Workshop on Hybrid Systems: Computation and Control (HSCC), 2005.
48. Second Asian Symposium on Programming Languages and Systems (APLAS), 2004.
49. 10th International Conference on Real-Time and Embedded Computing Systems and Applications (RTCSA), 2004.
50. Fourth Annual ACM Conference on Embedded Software (EMSOFT), 2004.
51. Eighth International Symposium on Formal Techniques in Real-Time and Fault-Tolerant Systems (FTRTFT), and Second International Workshop on Formal Modeling and Analysis of Timed Systems (FORMATS), 2004.
52. 31st International Colloquium on Automata, Languages, and Programming (ICALP), 2004.
53. Fourth International Conference on Application of Concurrency to System Design (ACSD), 2004.
54. Seventh International Workshop on Hybrid Systems: Computation and Control (HSCC), 2004.
55. First International Workshop on Formal Modeling and Analysis of Timed Systems (FORMATS), 2003.
56. Third International Symposium on Temporal Representation and Reasoning and Fourth International Conference on Temporal Logic (TIME-ICTL), 2003.

57. 18th Annual IEEE Symposium on Logic in Computer Science (LICS), 2003.
58. International Conference on Programming Language Design and Implementation (PLDI), 2003.
59. Eighth International Conference on Implementation and Application of Automata (CIAA), 2003.
60. Fifth International IFAC Conference on Analysis and Design of Hybrid Systems (ADHS), 2003.
61. Second International Workshop on Embedded Software (EMSOFT), 2002.
62. Seventh International Symposium on Formal Techniques in Real-Time and Fault-Tolerant Systems (FTRTFT), 2002.
63. 13th International Conference on Concurrency Theory (CONCUR), 2002.
64. 19th International Conference on Automated Deduction (CADE), 2002.
65. 14th International Conference on Computer-Aided Verification (CAV), 2002.
66. Second International Symposium on Temporal Representation and Reasoning (TIME), 2002.
67. Fifth International Workshop on Hybrid Systems: Computation and Control (HSCC), 2002.
68. First International Workshop on Theory and Practice of Timed Systems (TPTS), 2002.
69. Fifth International Conference on Coordination Models and Languages (COORDINATION), 2002.
70. 28th International Colloquium on Automata, Languages, and Programming (ICALP), 2001.
71. First International Symposium on Temporal Representation and Reasoning (TIME), 2001.
72. Second International Workshop on Distributed System Validation and Verification (DSVV), 2001.
73. Fourth International Workshop on Hybrid Systems: Computation and Control (HSCC), 2001.
74. 11th International Conference on Concurrency Theory (CONCUR), 2000.
75. 17th International Conference on Automated Deduction (CADE), 2000.
76. First International Workshop on Distributed System Validation and Verification (DSVV), 2000.
77. Fourth International Conference on Automation of Mixed Processes (ADPM), 2000.
78. Tenth International Conference on Concurrency Theory (CONCUR), 1999.
79. 11th International Conference on Computer-Aided Verification (CAV), 1999.
80. 26th International Colloquium on Automata, Languages, and Programming (ICALP), 1999.
81. 19th Annual IEEE Conference on Distributed Computing Systems (ICDCS), 1999.
82. Fifth AMAST Workshop on Real-Time Systems (ARTS), 1999.
83. Tenth International IEEE Symposium on Computer-Aided Control System Design (CACSD), 1999.
84. Second International Workshop on Formal Methods in Computer-Aided Design (FMCAD), 1998.
85. Fifth International Symposium on Formal Techniques in Real-Time and Fault-Tolerant Systems (FTRTFT), 1998.
86. 23rd International Symposium on Mathematical Foundations of Computer Science (MFCS), 1998.
87. Third International Workshop on the Verification of Infinite-State Systems (INFINITY), 1998.

88. 13th Annual IEEE Symposium on Logic in Computer Science (LICS), 1998.
89. Fourth International Conference on Tools and Algorithms for the Construction and Analysis of Systems (TACAS), 1998.
90. Fifth International Hybrid Systems Workshop (Notre Dame, Indiana), September 1997.
91. International Symposium on Theoretical Aspects of Computer Software (TACS), 1997.
92. Second International Workshop on the Verification of Infinite-State Systems (INFINITY), 1997.
93. Ninth International Conference on Computer-Aided Verification (CAV), 1997.
94. Fourth AMAST Workshop on Real-Time Systems (ARTS), 1997.
95. Formal Aspects of Software Engineering (FASE), 1997.
96. International Workshop on Hybrid and Real-Time Systems (HART), 1997.
97. First ACM SigPlan Workshop on the Automated Analysis of Software (Paris, France), January 1997.
98. Fourth International Hybrid Systems Workshop (Ithaca, New York), October 1996.
99. Fourth International Symposium on Formal Techniques in Real-Time and Fault-Tolerant Systems (FTRFT), 1996.
100. Seventh International Conference on Concurrency Theory (CONCUR), 1996.
101. Sixth International Conference on Concurrency Theory (CONCUR), 1995.
102. Seventh International Conference on Computer-Aided Verification (CAV), 1995.
103. Tenth Annual IEEE Symposium on Logic in Computer Science (LICS), 1995.
104. 13th Annual IEEE Conference on Distributed Computing Systems (ICDCS), 1993.

Member of Journal and Book Editorial Boards

1. EATCS Texts and Monographs Series, Springer, since 2004.
2. *Foundations and Trends in Electronic Design Automation*, NOW Publishers, since 2004.
3. *Theoretical Computer Science*, Springer, 2002–09.
4. *ACM Transactions on Embedded Computing Systems*, 2001–08.
5. *Software Tools for Technology Transfer*, Springer, 1997–2008.
6. *Formal Methods in System Design*, Springer, 1996–2008.
7. *Handbook of Networked and Embedded Control Systems*, Birkhäuser, 2005.
8. *International Journal of Embedded Systems*, special issue on Hardware-Software Codesign for Systems-on-Chip, 2004.

Journal Referee

ACM Transactions on Programming Languages and Systems; ACM Transactions on Software Engineering and Methodology; Communications of the ACM; Design Automation for Embedded Systems; Discrete-Event Dynamic Systems; Distributed Computing; Formal Aspects of Computing; Formal Methods in System Design; IEEE Transactions on Automatic Control; IEEE Transactions on Computers; IEEE Transactions on Software Engineering; Information and Computation; Information Processing Letters; International Journal of Systems Science; Journal of Automated Reasoning; Journal of Computer and Software Engineering; Journal of Logic and Computation; Journal of Parallel and Distributed Computing; Journal of the ACM; SIAM Journal on Computing; The Journal of Symbolic Computation; The Journal of Symbolic Logic; Theoretical Computer Science.

Book Referee

Addison-Wesley; Elsevier; IEEE Press; Kluwer Academic Publishers; MIT Press; Springer.

Conference Referee

IEEE Symposium on Logic in Computer Science, 1991; Conference on Computer-Aided Verification, 1992; ACM Symposium on Principles of Distributed Computing, 1992; Conference on Concurrency Theory, 1992; IEEE Symposium on Logic in Computer Science, 1993; Conference on Computer-Aided Verification, 1993; ACM Symposium on Principles of Distributed Computing, 1993; IEEE Symposium on Foundations of Computer Science, 1993; IEEE Real-Time Systems Symposium, 1993; Conference on Computer-Aided Verification, 1994; Conference on Formal Techniques in Real-Time and Fault-Tolerant Systems, 1994; IEEE Symposium on Foundations of Computer Science, 1994; Conference on Algebraic Methodology and Software Technology, 1995; Conference on the Mathematics of Program Construction, 1995; ACM Symposium on Principles of Distributed Computing, 1995; IEEE Symposium on Foundations of Computer Science, 1995; International Conference on Parallel Processing, 1996; Asian Computing Science Conference, 1996; Symposium on Theoretical Aspects of Computer Science, 1997; IEEE Symposium on Logic in Computer Science, 1997; International Colloquium on Automata, Languages, and Programming, 1997; High-Assurance Systems Engineering Workshop, 1997; IEEE Symposium on Foundations of Computer Science, 1997; IEEE Real-Time Systems Symposium, 1997; Symposium on Theoretical Aspects of Computer Science, 1998; IEEE International Symposium on Circuits and Systems, 1998; IEEE Conference on Decision and Control, 1998; IEEE Symposium on Foundations of Computer Science, 1998; Conference on Algebraic Methodology and Software Technology, 1998; ACM Symposium on Principles of Programming Languages, 1999; Conference on Tools and Algorithms for the Construction and Analysis of Systems, 1999; Static Analysis Symposium, 1999; Conference on Computer Science Logic, 1999; ACM Symposium on Principles of Programming Languages, 2000; IEEE Symposium on Logic in Computer Science, 2000; European Conference on Parallel Computing, 2000; IEEE Symposium on Foundations of Computer Science, 2000; ACM Symposium on Principles of Programming Languages, 2001; Conference on Concurrency Theory, 2001; IEEE Symposium on Foundations of Computer Science, 2001; ACM Symposium on Principles of Programming Languages, 2002; IEEE Symposium on Logic in Computer Science, 2002; IEEE Symposium on Foundations of Computer Science, 2002; Workshop on Hybrid Systems: Computation and Control, 2003; ACM Conference on Languages, Compilers, and Tools for Embedded Systems, 2003; Conference on Concurrency Theory, 2003; IEEE Conference on Decision and Control, 2003; IEEE Symposium on Logic in Computer Science, 2004; IEEE Conference on Decision and Control, 2004; Symposium on Theoretical Aspects of Computer Science, 2005; Foundations of Software Technology and Theoretical Computer Science, 2005; Conference on Verification, Model Checking, and Abstract Interpretation, 2006; ACM Symposium on Principles of Programming Languages, 2008; ACM Conference on Programming Language Design and Implementation, 2008.

Other Referee

ACM Distinguished Dissertation Award; Agence Nationale de la Recherche, France; Army Research Office; Austrian Fonds zur Förderung wissenschaftlicher Forschung; Christian-Doppler Forschungsgesellschaft, Austria; Czech Science Foundation; Deutsche Forschungsgemeinschaft; Dutch Technology Foundation STW; German-Israeli Foundation for Scientific Research and Development; GULP Dissertation Award, Italy; INRIA, France; Israel Science Foundation; Italian Ministry for Education, Universities, and Research; NASA Technical Memoranda; National Science Foundation; Netherlands Science Research Foundation; Otto-Hahn Medal of the Max-Planck Society; Science Foundation of Ireland; Springer Lecture Notes in Computer Science; Swedish Research Council for Engineering Sciences; Swiss National Science Foundation; UK Engineering and Physical Sciences Research Council; University of California at Berkeley Extension; University of California MICRO Program; Volkswagen Stiftung, Germany.

Other Technical Committees

Max-Planck Institute for Software Systems, member, scientific advisory board, since 2008.

International Workshop on Formal Methods in Systems Biology (FMSB), founding member, steering committee, since 2008.

Computer-Aided Verification (CAV), chair, award committee, 2008.

European Research Training Network on Games and Automata for Synthesis and Validation (GAMES), member, steering committee, since 2006.

Annual IEEE Symposium on Logic in Computer Science (LICS), member, advisory board, since 2006.

Annual ACM Conference on Embedded Software (EMSOFT), chair, advisory board, since 2005.

Annual ACM Conference on Embedded Software (EMSOFT), founding member and chair, steering committee, 2001–2005.

ACM Special Interest Group on Embedded Systems (SigBed), founding member, executive board, 2002–2005.

QPQ Deductive Software Repository, member, advisory board, since 2002.

First Sino-Euro-American Workshop on the Scientific Foundations of Informatics as an Engineering Discipline (Macao), member, steering committee, 2001.

First NASA HDCC Workshop on High Dependability Computing (Mountain View, California), invited participant, working group on Formal Methods, 2001.

Third IMACS International Multiconference on Circuits, Systems, Communications, and Computers (Athens, Greece), member, scientific committee, 1999.

International Workshop on Current Trends in Applied Formal Methods (Boppard, Germany), member, advisory board, 1998.

International Workshop on Hybrid Systems: Computation and Control, founding member, steering committee, 1997–2000.

DARPA ISAT Meeting, invited participant, working group on Complex Systems, 1997.

ACM Workshop on Strategic Directions in Computing Research, invited participant, working group on Concurrency and working group on Formal Methods, 1996.

Member of Professional Organizations

American Association for the Advancement of Science.

Association for Computing Machinery (SigAct, SigBed, SigDA, SigPlan).

Association for Symbolic Logic.

European Association for Theoretical Computer Science.

IEEE (Computer Society).

Sigma Xi.

Consulting Activities

Wind River Systems, Alameda, California, 2002.

Scientific Systems Company, Woburn, Massachusetts, 2001.

Digital Equipment Corporation Systems Research Center, Palo Alto, California, 1996–1999.

GammaTech, Ithaca, New York, 1995–1996.